

Dual-Mode Silicon Ring Resonator Based on Subwavelength Grating Directional Couplers

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Abstract—We propose and experimentally demonstrate a dual-mode silicon ring resonator which could process TE₀ and TE₁ modes simultaneously. By introducing subwavelength grating into the coupling region, compact footprint of $\sim 90 \mu\text{m} \times 20 \mu\text{m}$ is achieved. The proposed device is fabricated on the standard silicon-on-insulator platform. The measured results indicate the mode crosstalk within C band are less than -12.2 and -11.5 dB for TE₀ and TE₁ modes, respectively. Furthermore, heaters for resonant wavelength tuning are fabricated and measured with good uniformity for both modes.

Index Terms—Mode multiplexing, subwavelength grating, silicon ring resonator.

I. INTRODUCTION

IN THE past decade, silicon photonics is commonly regarded as a promising solution for inter/intra data-center connections due to its advantages as low-cost, small footprint and compatibility with matured Complementary Metal-Oxide Semiconductor (CMOS) technology [1], [2]. To meet the increasing capacity requirement of optical interconnection, multiplexing technologies such as the wavelength division multiplexing (WDM), polarization division multiplexing (PDM) are intensively utilized [3], [4], [5], [6]. Nowadays, mode division multiplexing (MDM) technology attracts more and more attention due to its compatibility with other multiplexing technologies and ability on further increasing the transmission capacity [7]. In order to build a chip-level MDM network, a variety of fundamental building blocks, including mode multiplexer [8], multi-mode crossing [9], coupler [10], [11], [12], [13], have been extensively investigated. Advanced circuits, such as multi-mode switch and router, are further proposed and demonstrated [14], [15].

Due to the severe mode dispersion in silicon multimode waveguides, demultiplexing-process-multiplexing architecture is commonly used for MDM signal processing, resulting in a high-loss and inefficient layout. Recently, a novel multi-mode

signal processing architecture attracts more and more attention for its ability on processing MDM signals simultaneously [16], [17]. Thus, MDM signal processing circuits with high performance and small footprint could be achieved.

Micro-ring resonator (MRR) as an essential element in silicon integrated circuit, is widely utilized as filter [18], modulator [19] and sensor [20] in single-mode regime. With the development of MDM technology, MRR that could process multi-mode signals is highly desired. However, it is difficult to achieve a multi-mode MRR with compact footprint, due to the severe mode dispersion in the coupling region. Reported MRR [21] used a conventional directional coupler to achieve uniform two-mode coupling with a racetrack length of $95 \mu\text{m}$, resulting an inefficient layout. MRR based on free space geometric optics [22] could overcome the mode dispersion and support more modes. However low Q-factor limits its application in practical system.

In this paper, we propose and experimentally demonstrate a dual-mode MRR with compact footprint. By introducing subwavelength grating [23] into the coupling region, length of the racetrack is reduced to $20 \mu\text{m}$. The proposed device is fabricated on the standard silicon-on-insulator (SOI) platform. The measured results indicate the mode crosstalk within C band are less than -12.2 and -11.5 dB for TE₀ and TE₁ modes, respectively. Furthermore, heaters for resonant wavelength tuning are measured with good uniformity for both modes.

II. DESIGN AND SIMULATION

The scheme of the proposed device is shown in Fig. 1(a). The device consists of two 180° bends and two coupling regions. TE₀ and TE₁ modes at resonance wavelengths are coupled into the MRR through the input port and coupled out at the drop port. Coupling regions are designed as the SWG waveguides, which could enhance the coupling efficiency to realize compact coupling length. Bend radius is designed as $45 \mu\text{m}$ to support TE₀ and TE₁ modes transmission with low crosstalk. Thus, uniform process on TE₀ and TE₁ modes could be achieved. Design details of the SWG waveguides and multi-mode bends are introduced in following sections.

A. Subwavelength Grating Directional Coupler

Fig. 1(b) shows the top view of the proposed SWG waveguide. W represents the waveguide width. In order to support transmission of both TE₀ and TE₁ modes, W is chosen to be $1 \mu\text{m}$. While Λ and η stand for the period and duty cycle of the subwavelength structure. To realize sufficient coupling for both

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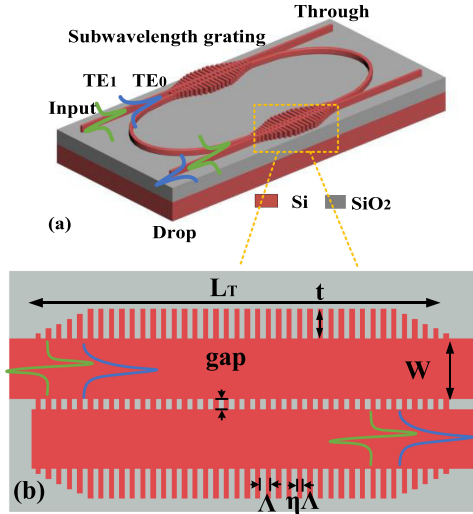


Fig. 1. (a) Schematic of the proposed mode-insensitive MRR; (b) Top view of the SWG waveguides.

TE₀ and TE₁ modes, Λ and η need to be carefully designed to meet both the SWG working condition and fabrication rules.

According to the equivalent dielectric theory [24], SWG waveguide could be regarded as a uniform equivalent dielectric material. The equivalent refractive index n_{eq} is given by

$$n_{eq}^2 = \eta \cdot n_{si}^2 + (1 - \eta) \cdot n_{sio2}^2 \quad (1)$$

n_{si} and n_{sio2} are the refractive indices of the silicon and silica. To meet the SWG working condition [24], the period Λ needs to be designed as

$$\Lambda < \lambda_{min} / (2 \cdot n_{eq}) \quad (2)$$

λ_{min} stands for minimum operating wavelength. To make the device work within C band, Λ and η are chosen as 280 nm and 0.5, respectively. Coupling gap is chosen as 220 nm. Furthermore, a SWG based taper with width of 600 nm and length of 1.4 μm is introduced to realize adiabatic mode coupling between channel waveguide and SWG waveguide.

Fig. 2(a) shows the simulated coupling efficiencies with different coupling length at 1550 nm for TE₀ and TE₁ modes. To achieve uniform light coupling for dual-mode signal process, coupling length is chosen to be 20 μm . Insertion losses of TE₀ and TE₁ modes are simulated to be ~ 0.26 dB and 0.4 dB, respectively. While mode crosstalk is simulated as ~ -18 dB for both TE₀-TE₁ and TE₁-TE₀. The simulated light propagating evolution in SWG waveguides are shown in Fig. 2(b) and (c). To be noted, resonance of the mode field is caused by the Bloch-Floquet modes of the SWG structure [25], which could be reduced by selecting a smaller Λ .

B. Bend Waveguide

In order to reduce the mode crosstalk between TE₀ and TE₁ modes, arc waveguides with radius of 45 μm is usually used to form the dual-mode bends. Simulated transmission spectra of the bends are shown in Fig. 3(a), (b) and (c) illustrate the simulated light propagation for TE₀ and TE₁ modes.

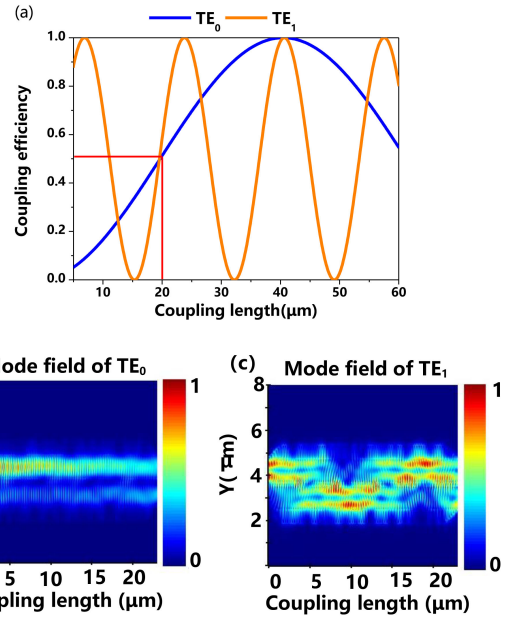


Fig. 2. (a) Simulated coupling efficiencies with different coupling length at 1550 nm for TE₀ and TE₁ modes and the light propagating evolution in the proposed SWG waveguides for (b) TE₀ and (c) TE₁ modes.

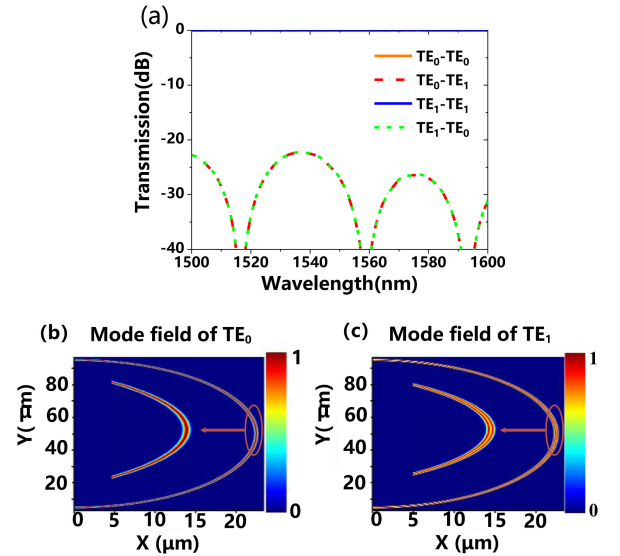


Fig. 3. (a) Simulated transmission spectra of the bent waveguide for TE₀ and TE₁ modes. Simulated light propagation for (b) TE₀ and (c) TE₁ modes.

In the legend “TE₀-TE₁”, the first “0” stands for the input TE₀ mode, while the second “1” stands for the output TE₁ mode. Results indicate insertion loss for TE₀ and TE₁ modes are ~ 0.01 and 0.05 dB, respectively. While the mode crosstalk between TE₀ and TE₁ modes are < -22.2 dB within the C band.

C. Dual-Mode Ring Resonator

Combining the two key elements discussed above, the dual-mode MRR can be formed. In order to characterize the proposed device, the drop port transmission spectra for TE₀ and TE₁ modes are further simulated, as shown in Fig. 4. The simulation

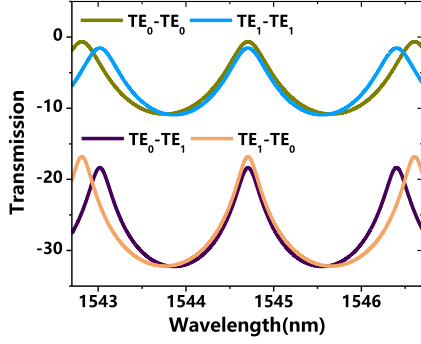


Fig. 4. Simulated the drop port transmission spectra of the proposed MRR for TE_0 and TE_1 modes.

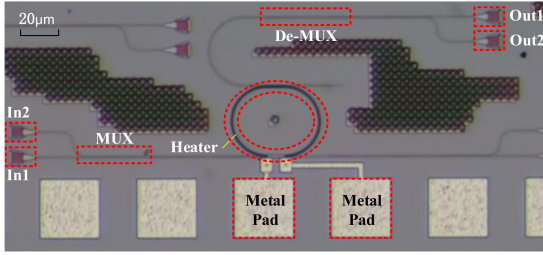


Fig. 5. Microscope image of the fabricated device.

results indicate that bandpass filtering for dual-mode signals is successfully achieved. The insertion loss for TE_0 and TE_1 modes are simulated to be ~ 0.67 and 1.58 dB. While the mode crosstalk are ~ -17.7 and -15.3 dB for TE_0 and TE_1 modes, respectively. Free spectrum ranges (FSRs) for TE_0 and TE_1 modes are simulated to be 1.9 and 1.7 nm. This can be increased by optimizing the bent waveguide to reduce its size.

III. FABRICATION

The proposed device is fabricated on a SOI wafer with a 220 nm-thick top-silicon layer and SiO_2 buried layer of $2 \mu m$. Fig. 5 shows the microscope image of the fabricated device. 248 nm deep ultraviolet photolithography and inductively coupled plasma (ICP) etching are used to form the waveguide structure. To form the channel waveguide, full etching is adopted. The waveguide width is chosen to be $1 \mu m$ to support dual-mode transmission. Λ and η of the SWG waveguide are chosen as 280 nm and 0.5 , respectively. To achieve uniform dual-mode processing, coupling length and gap of the SWG coupling region are set as $20 \mu m$ and 220 nm. Furthermore, an adiabatic taper with width of 600 nm and length of $1.4 \mu m$ was introduced to realize adiabatic mode coupling between channel waveguide and SWG waveguide. The bending radius of the MRR is set to be $45 \mu m$. In order to adjust the resonance wavelength of the ring, an integrated TiN heater is fabricated on top of the ring. The thickness and the width of the heater are 200 nm and $5 \mu m$, respectively. The grating couplers (GCs) are used to couple light into and out of the chip. The circuit is fabricated at the Advanced Micro Foundry (AMF) in Singapore. Footprint of the fabricated MRR is $90 \mu m \times 20 \mu m$. In order to test the proposed MRR, adiabatic mode MUX and De-MUX are adopted. The insertion

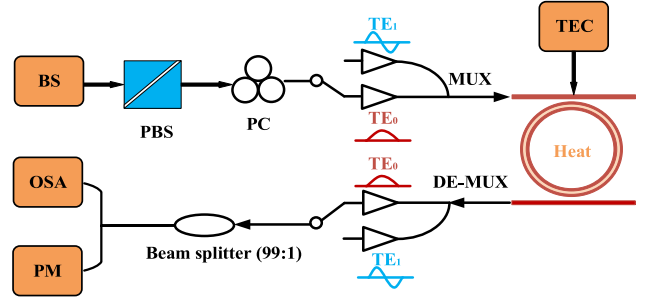


Fig. 6. The experiment setup.

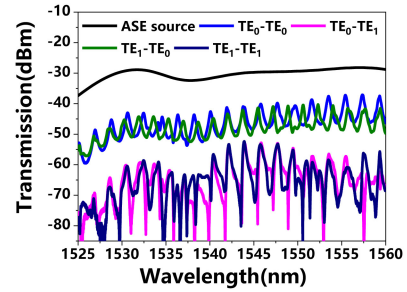


Fig. 7. Measured transmission spectra of the MRR for TE_0 and TE_1 modes input from 1530 to 1565 nm.

losses for TE_0 - TE_1 modes are ~ 1.15 and 1.18 dB, respectively, and the mode crosstalk is ~ -16 dB in the wavelength range of 1530 – 1565 nm.

IV. MEASUREMENT

The experiment setup is shown in Fig. 6. In order to verify the performance of the fabricated device, the transmission spectra over C band of the fabricated device is measured, by introducing the light from a broadband source (BS). Assisting by the polarization beam splitter (PBS) and polarization controller (PC), linearly polarized light input can be achieved. The chip is set on the coupling test platform. With the help of Thermo Electric Cooler (TEC), temperature of the test environment is kept at $25 \pm 0.01^\circ C$. On-chip heater is used to tune the resonance wavelength for both TE_0 and TE_1 modes. A fiber based beam splitter is utilized to split 1% of the output light into power meter (PM) for power monitoring, and 99% of the output light is launched into the optical spectrum analyzer (OSA) for transmission spectra measurement. The light input from port In1 stands for the input TE_0 mode. The transmission spectrum of TE_0 is measured at port Out1, and the crosstalk is measured at port Out2. Details of the measured results are demonstrated and discussed in the following section.

V. RESULTS AND DISCUSSION

Transmission spectra within C band for TE_0 and TE_1 modes are measured. The results are shown in Fig. 7.

The total losses of the fabricated circuit for TE_0 and TE_1 modes are measured to be 13 and 14.73 dB. Coupling loss of the grating coupler fabricated on the same chip is measured to

TABLE I
THE SIMULATION AND EXPERIMENT RESULTS

FOM	ER(dB)		FSR(nm)		Crosstalk(dB)		Q factor	
Mode	TE ₀	TE ₁	TE ₀	TE ₁	TE ₀	TE ₁	TE ₀	TE ₁
Simulation	10.2	9.4	1.9	1.7	-17.7	-15.3	3860	7644
Experiment	9.4	8.3	1.912	1.704	-12.1	-10.3	3300	5400

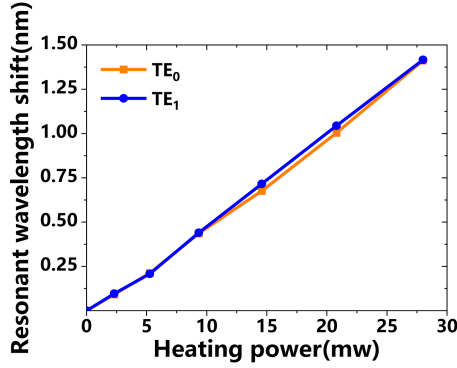


Fig. 8. Measured resonant wavelength shift with heating power from 0 to 28 mw for TE₀ and TE₁ modes.

be ~ 5 dB. Thus the insertion losses of the fabricated MRR are ~ 3 and 4.73 dB for TE₀ and TE₁ modes, respectively. The FSRs for TE₀ and TE₁ modes are measured to be 1.912 and 1.704 nm. The quality factors (Q-factors) for TE₀ and TE₁ modes are measured to be ~ 3300 and 5400, respectively. The crosstalk for TE₀ and TE₁ modes are measured to be < -12.1 and -10.3 dB. To be noted, relatively low extinction ratio (~ 8.3 dB) is due to the strong coupling in SWG waveguides, which could be improved by selecting a suitable coupling gap.

To make a clear comparison of the figure of merit (FOM) between simulation and experiment results. Extinction ratio (ER), FSR and crosstalk for TE₀ and TE₁ modes are collected and shown in Table I. The results indicate good consistency between simulation and measurement. Furthermore, uniformities of these FOMs indicate good performance of the proposed dual-mode signal processing. To be noted, the slight performance degradation is due to the imperfect fabrication process.

In order to adjust the resonance wavelength of the ring, an integrated TiN heater is fabricated on top of the MRR. To characterize the performance of the heater, resonant wavelength with different heating powers for TE₀ and TE₁ modes are measured respectively. The measured results are shown in Fig. 8. The tuning efficiency for TE₀ and TE₁ modes are measured to be 0.0504 and 0.0505 nm/mw, respectively.

VI. CONCLUSION

In summary, we propose and experimentally demonstrate a dual-mode silicon ring resonator which could process TE₀ and TE₁ modes simultaneously. By introducing subwavelength grating into the coupling region, compact footprint of $\sim 90 \mu\text{m} \times 20 \mu\text{m}$ is achieved. Measured results show good multi-mode processing is successfully achieved. The proposed device could be utilized in advanced mode division multiplexing network.

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