

Compositional Information-flow Security for Interactive Systems

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Abstract—To achieve end-to-end security in a system built from parts, it is important to ensure that the composition of secure components is itself secure. This work investigates the compositionality of two popular conditions of possibilistic noninterference. The first condition, *progress-insensitive noninterference* (PINI), is the security condition enforced by practical tools like JSFlow, Paragon, sequential LIO, Jif, FlowCaml, and SPARK Examiner. We show that this condition is not preserved under fair parallel composition: composing a PINI system fairly with another PINI system can yield an insecure system. We explore constraints that allow recovering compositionality for PINI. Further, we develop a theory of compositional reasoning. In contrast to PINI, we show what PSNI behaves well under composition, with and without fairness assumptions. Our work is performed within a general framework for nondeterministic interactive systems.

I. INTRODUCTION

Modularity and compositionality are essential for the design and construction of modern computing systems. A major challenge is *secure composition*: to achieve end-to-end security in a system built from parts, it is important to ensure that the composition of secure components is itself secure. Secure composition is particularly intricate because security conditions are often fragile under system behavior modifications. Adding, removing, or modifying a single trace or event can break the security of a system [32].

This paper studies the foundations of secure composition. Our focus is on specifying *confidentiality* (or dual flavors of integrity [9], [10]) by defining what constitutes secure *information flow* through computing systems.

a) State of the art in security for interactive systems:

Given the importance of the subject, it is not surprising that literature has explored security of communicating systems and their composition in assorted settings, discussed in detail in Section VI. Unfortunately, the models underlying previous frameworks on formalising and enforcing secure information flow [4], [12], [21], [34], [37], [42], [51] are all very different, and largely lacking relative comparison. Models range from memory-to-memory transformers, memory-to-output-stream transformers, input-to-output-stream transformers, and input-output-trace emitters. The frameworks make different fundamental assumptions, e.g. determinism, synchronous communication, environment totality, and fixed interaction pattern, leading to different notions of observation and environment.

For example, security has been addressed in *reactive* systems (e.g., [2], [12], [43]). The reactive system models considered exercise a restrictive pattern of communication, where the system waits for an input, and once an input has been received it proceeds with executing an appropriate handler until completion, possibly producing some output on the way. *Event*

systems have been a focus of several previous approaches (e.g., [28], [29], [54], [57]). Events in the early work have different levels of sensitivity with the goal of protecting both presence and content of events but not distinguishing between the two. Interaction patterns in some of this work are fixed; Wittbold and Johnson [54] assume the pattern of receiving a secret and a public event, followed by sending a secret and public event. With *process calculi* as the underlying models, a line of work (e.g., [19], [22], [23], [40], [46], [47]) studies secure interaction, inheriting concrete features from the process calculus and not distinguishing between the sensitivity of presence and content of events. More general, and closer to our work, are formalizations that operate on *labeled transition systems* [16], [37], [42]. However, these model environments as strategies, which are separate from the computational model. Strategies are total, i.e., can always receive and send messages.

Thus, key questions that have not been addressed by previous work are: (i) *what is an appropriate general model of security of interactive systems*, (ii) *how to distinguish sensitivity of message presence and content in such a model*, (iii) *how do we model environments as part of the system*, and (iv) *how do we provide flexible ways for composing systems*.

b) Progress-sensitive and progress-insensitive security:

The focus of our work is two popular security conditions. The first condition, *progress-insensitive noninterference* (PINI) [3], [4], [12], prevents information leaks from secret sources to public sinks, but allows secrets to affect progress of public computation. Thanks to the liberty it provides for handling loop constructs, this condition is a popular target for such practical security tools as JSFlow [21], Paragon [14], sequential LIO [51], Jif [34], FlowCaml [50], and SPARK Examiner [6]. The second condition is *progress-sensitive noninterference* (PSNI) [4], [12], [37], [43], which does not allow leaks via progress. The advantage of PSNI is that it provides stronger security guarantees that is not susceptible to laundering secrets by brute-force attacks [3] or re-running programs [11]. An important question that has not been answered by previous work is (v) *how do PINI and PSNI behave under composition*?

c) Contributions:

Motivated by questions (i)-(v), this paper delivers the following contributions. The first contribution is a *general security framework* to model security of interactive systems. Given the diversity of previous work, our goal is to avoid “another information-flow model” with no relation to previous approaches. This motivates us to *systematize and generalize* the work in the area so far. We obtain full generality by adopting labeled transition systems as the underlying model. Our asynchronous input-output-stream emitters (Definition II.6) can model nondeterminism, non-blocking input, and arbitrary interaction patterns. In contrast

to previous work, environments are *interactive systems*, a significant generalization of previous work. The expressiveness of the environment model is key for compositionality results. Unifying the assumptions on environments and systems provides us with a generic system model. More importantly, it also paves the way for secure composition thanks to the possibility that systems and environments can be manipulated interchangeably. The second contribution is *combinators* for composing systems. The combinators allow flexibility in how exactly composed components can interact with each other. This is in contrast to previous work where composition is typically restricted to a single way. The third contribution allows us further generality in the modeling of interaction: we distinguish between the sensitivity of message *presence* and message *content* without restricting communication paradigms. The fourth contribution is the study of *compositionality for PINI and PSNI*. We find that PINI is not preserved under fair parallel composition: composing a PINI system fairly with another PINI system can yield an insecure system, and thus, compositionality of PINI relies fundamentally on unfairness. We explore constraints that allow recovering compositionality for PINI. Further, we develop a theory of compositional reasoning. In contrast to PINI, we show what PSNI behaves well under composition, with and without fairness assumptions.

d) Organization: The rest of the paper proceeds as follows. Section II presents the general setting of interactive systems, as specified by labeled transition systems. Section III presents the security definitions. Section IV establishes compositionality properties for a core of combinators, with and without fairness assumptions. Section V demonstrates the generality of our results by providing a rich language of secure combinators. Section VI reports on related work. Section VII offers conclusions and points to worthwhile future work.

II. INTERACTIVE SYSTEMS

We present a language-independent framework for reasoning about the behavior of interactive programs. The framework functions as the foundation for our technical contribution, and unifies several previous frameworks for interactive program security [12], [16], [37], [42], [43].

A. Computation Model

Our model of computation is a *labeled transition system* (LTS). An LTS is a triple $(\mathbb{P}, \mathbb{A}, \rightarrow)$; \mathbb{P} is a set (of *processes*), ranged by p , \mathbb{A} is a set (of *action labels*), ranged by a , and $\rightarrow \subseteq \mathbb{P} \times \mathbb{A} \times \mathbb{P}$ (a *labeled transition relation*). Computation occurs in discrete steps (transitions). The label on a step is the *effect* of said step. These effects are the *only* external interface to our processes; they are “black boxes” in every other respect. $p \xrightarrow{a} p'$ iff $(p, a, p') \in \rightarrow$, and $p \xrightarrow{a}$ iff $p \xrightarrow{a} p'$ for some p' .

The processes we consider interact with their environment through channel-based message-passing. They have two kinds of effects: (message-)input, denoted i , and (message-)output, denoted o . A message, m , is a value v on a channel c .

$$a ::= i \mid o \quad i ::= ?m \quad o ::= !m \quad m ::= cv \quad (\text{act})$$

Here, $!cv$ (resp. $?cv$) denotes a message sent (resp. received) on channel c carrying value v . We let i , o , m , c and v range over \mathbb{I} , \mathbb{O} , \mathbb{M} , \mathbb{C} and \mathbb{V} , respectively.

Definition II.1. An *input-output LTS* (LTS_{IO}) is an LTS with action labels ranged by a as defined in (act). \diamond

This notion of computation applies to a wide range of programs and systems. For instance, Bohannon et al. give the semantics of a JavaScript-like language as an LTS_{IO} in [12], [13], and Rafnsson et al. give the semantics of an imperative language with I/O as an LTS_{IO} in [42], demonstrating that LTS_{IO} are rich enough to model anything from batch-style programs which receive one i , produce one o and terminate, to reactive systems which continuously interact. Since all of our results apply to LTS_{IO} , our contributions are general.

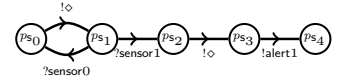
To illustrate, the following program reads binary sensor values until receiving 1, after which it outputs an alert message. Next to it is a graph of an LTS_{IO} describing its behavior.

repeat

in sensor b

until $b = 1$

out alert 1



To model noninteraction (e.g. evaluating a loop condition), we distinguish a channel for internal actions, and denote a message on it by \diamond . The program receiving value 0 on channel sensor, then receiving a 1, and subsequently outputting 1 on alert, is then represented by the following transition sequence.

$$p_{s_0} \xrightarrow{!o} p_{s_1} \xrightarrow{?sensor0} p_{s_0} \xrightarrow{!o} p_{s_1} \xrightarrow{?sensor1} p_{s_2} \xrightarrow{!o} p_{s_3} \xrightarrow{!alert1} p_{s_4}$$

B. Behaviors

To study the behavior of an LTS_{IO} , we consider sequences of actions performed by it.

Traces \mathbb{T} , ranged over by t , are (finite) lists of actions. For instance, with “ ϵ ” denoting the empty trace and “ \cdot ” the *constructor* (“cons”) operator, $t_s = !o. ?sensor1. !o. !alert1. \epsilon$ is a trace. Alternatively, \mathbb{T} could be defined as $\mathbb{T} = \mathbb{A}^*$, i.e., the Kleene closure of \mathbb{A} , or *inductively*, by asserting that $\epsilon \in \mathbb{T}$, and specifying, given an action a and a trace $t \in \mathbb{T}$, how to obtain a new trace in \mathbb{T} ($a.t \in \mathbb{T}$). For simplicity, we usually omit the ϵ trailing a nonempty trace, use “ \cdot ” to also denote trace concatenation ($\epsilon.t = t$ and $(a.t').t = a.(t'.t)$), and write t^n for the concatenation of t n times ($t^0 = \epsilon$ and $t^{n+1} = t.t^n$). We write $t \leq t'$ when $t' = t.t''$ for some t'' .

Streams \mathbb{S} , ranged over by s , are (strictly) infinite lists of actions (\mathbb{S} exists and is unique [7]). For instance, with “ \cdot ” denoting the *destructor* operator, s_s for which $(\cdot) s_s = (!o, \hat{s}_s)$ and $(\cdot) \hat{s}_s = (?sensor0, s_s)$ is a stream, i.e., the infinite repetition of action sequence $!o. ?sensor0$. Alternatively, \mathbb{S} could be defined as $\mathbb{S} = \mathbb{A}^\omega$, i.e., the language of all infinite words over \mathbb{A} , or *coinductively*, by specifying, given a stream $s \in \mathbb{S}$, how to obtain an action and a new stream in \mathbb{S} (apply “ \cdot ” on s). For simplicity, we let $a.s$ denote any stream s' for which $(\cdot) s' = (a, s)$, and let $t^\omega = t.(t^\omega)$. Define $t \leq s$ and $t.s$ the same way as for traces. For more on coinduction, see [24].

The behaviors of processes are as follows. For each p , $p \xrightarrow{\epsilon} p$, and $p \xrightarrow{a.t}$ if $p \xrightarrow{a} p' \xrightarrow{t}$. Likewise, $p \xrightarrow{a.s}$ only if $p \xrightarrow{a} p' \xrightarrow{s}$. Let $\mathbb{T}(p) = \{t \mid p \xrightarrow{t}\}$ and $\mathbb{S}(p) = \{s \mid p \xrightarrow{s}\}$ be the set of traces and streams of p , i.e., the *trace- and stream-semantics* of p . For instance, $t_s \in \mathbb{T}(p_{s_0})$ and $s_s \in \mathbb{S}(p_{s_0})$, since $p_{s_0} \xrightarrow{!o}$ and $p_{s_0} \xrightarrow{?sensor0}$. Traces and streams can *themselves* be regarded as LTS_{IO} , simply by defining $a.t \xrightarrow{a} t$ and $a.s \xrightarrow{a} s$.

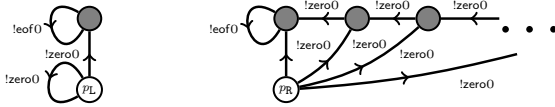
Due to the nature of PINI and due to the observation in Section III-D, we focus on stream semantics in this paper, which, under the right assumptions, are at least as expressive as trace semantics. First, the following is easy to see.

$$\forall s. s \in \mathbb{S}(p) \implies (\forall t \leq s. t \in \mathbb{T}(p)). \quad (s\text{-all-}t)$$

However, the following does *not* hold in general.

$$\forall t. t \in \mathbb{T}(p) \implies (\exists s \leq t. s \in \mathbb{S}(p)). \quad (t\text{-some-}s)$$

For instance, $\mathbb{S}(p_0^s) = \{s_s\}$ and $t_s \not\leq s_s$. So in general, stream semantics are less expressive than trace semantics. However, this is only the case for LTS_{IO} with *terminal states* (e.g. p_{s_4}). For LTS_{IO} with no terminal states, i.e., *productive* LTS_{IO} (formalized in Definition VI.1), we have (*t-some-s*). However, even if we assume LTS_{IO} are productive, trace- and stream-semantics are not equally expressive; the reverse implication of (*s-all-t*) does *not* hold in general. Consider these LTS_{IO} .



Here, $\mathbb{T}(p_L) = \mathbb{T}(p_R)$. However, $\mathbb{S}(p_L) \neq \mathbb{S}(p_R)$; in particular, for $s_{0s} = !\text{zero}0^\omega$, $p_L \xrightarrow{s_{0s}}$ but $p_R \not\xrightarrow{s_{0s}}$, since p_R always performs an arbitrary *finite* number of $!\text{zero}0$ actions. So there are scenarios where trace semantics is less expressive than stream semantics; trace semantics, depending on perspective, hides the eventuality of $!\text{eof}0$ in p_R , or hides its possible non-eventuality in p_L . However, in productive LTS_{IO} , this difference in expressiveness occurs *only* in the presence of *unbounded nondeterminism* (by König's lemma) [45], present in p_R as it nondeterministically picks a number n from the full range of natural numbers, in a single computation step.

We assume LTS_{IO} are productive, wlg. since any LTS_{IO} can be modeled as a productive LTS_{IO} , as follows. We model termination as an action by distinguishing a termination channel, and denoting any message on said channel by \star . The *obituary* wrapper O announces termination of the wrapped process.

$$\frac{p \not\rightarrow}{O(p) \xrightarrow{! \star} p} \quad \frac{p \xrightarrow{a} p'}{O(p) \xrightarrow{a} O(p')}$$

The *zombie* wrapper Z keeps terminated processes productive by enabling the $!\diamond$ action in terminal states.

$$\frac{p \not\rightarrow}{Z(p) \xrightarrow{! \diamond} Z(p)} \quad \frac{p \xrightarrow{a} p'}{Z(p) \xrightarrow{a} Z(p')}$$

Now any property ϕ on $\mathbb{T}(p)$ can be stated as a property ϕ' on $\mathbb{S}(Z(O(p)))$ such that $\phi(\mathbb{T}(p))$ iff $\phi'(\mathbb{S}(Z(O(p))))$, since $\mathbb{T}(p) = \{t \mid (\exists s \in \mathbb{S}(Z(O(p)))) . t \leq s\} \wedge (\forall t'. t' . ! \star \not\leq t)\}$.

C. Comparing Behaviors

To reason about the behavior of processes, we need ways to compare behaviors. Comparing traces is easily done component-wise, and a proof of trace equality is inductive (thus finite). What about infinite streams? One way is to define stream *equivalence* as non-difference (nonexistence of a derivation in an inductively defined difference relation). We instead use a coinductive definition which captures the idea of component-wise trace equality (note that $\forall s. \exists ! a, s'. s \xrightarrow{a} s'$).

Definition II.2. $\mathcal{R} \subseteq \mathbb{S} \times \mathbb{S}$ is a *strong stream relation* iff

$$\begin{aligned} \forall s_1, s_2. s_1 \mathcal{R} s_2 &\implies (s_1 \not\rightarrow \wedge s_2 \not\rightarrow) \vee \\ \exists a, s'_1, s'_2. s_1 &\xrightarrow{a} s'_1 \wedge s_2 \xrightarrow{a} s'_2 \wedge s'_1 \mathcal{R} s'_2. \end{aligned}$$

s_1 and s_2 are *strongly stream related*, $s_1 = s_2$, iff there exists a strong stream relation \mathcal{R} such that $s_1 \mathcal{R} s_2$. \diamond

This is a strong bisimulation on streams, and thus an equivalence relation, stipulating component-wise equality. Roughly, the inductive definition of $t_1 = t_2$ is Definition II.2 with the implication reversed ($t_1 = t_2$ if $t_1 \not\rightarrow \wedge t_2 \not\rightarrow$ or $t_1 \xrightarrow{a} t'_1 \wedge t_2 \xrightarrow{a} t'_2 \wedge t'_1 = t'_2$). For $s_{0e} = !\text{zero}0.!\text{eof}0^\omega$, $s_{0s} \neq s_{0e}$. An inductive proof of difference would use the fact that $!\text{zero}0.!\text{zero}0 \leq s_{0s}$ and $!\text{zero}0.!\text{zero}0 \not\leq s_{0e}$; this leads us to the following useful observation, proven by Park in [39].

$$s_1 = s_2 \quad \text{iff} \quad \forall t. t \leq s_1 \iff t \leq s_2. \quad (= \text{-and-} ts)$$

Ultimately, we need to compare *observable* behavior to reason about information-flow security of processes. We define observables in Section III; for now, let \bullet range over unobservable actions. Furthermore, we let \bullet function as a wildcard (multiple occurrences of \bullet in the same context can represent different mathematical objects). *Observational equivalence* then becomes component-wise equality of observables. To obtain the next observable action, we use the following “weak” labeled transition relation: $p \xrightarrow{a} p'$ iff $a \neq \bullet \wedge (p \xrightarrow{a} p' \vee (p \xrightarrow{\bullet} \hat{p} \wedge \hat{p} \xrightarrow{a} p'))$.

Definition II.3. $\mathcal{R} \subseteq \mathbb{S} \times \mathbb{S}$ is a *weak stream relation* iff

$$\begin{aligned} \forall s_1, s_2. s_1 \mathcal{R} s_2 &\implies (s_1 \not\rightarrow \wedge s_2 \not\rightarrow) \vee \\ \exists a, s'_1, s'_2. s_1 &\xrightarrow{a} s'_1 \wedge s_2 \xrightarrow{a} s'_2 \wedge s'_1 \mathcal{R} s'_2. \end{aligned}$$

s_1 and s_2 are *weakly stream related*, $s_1 \simeq s_2$, iff there exists a weak stream relation \mathcal{R} such that $s_1 \mathcal{R} s_2$. \diamond

This is CP-similarity [12]; a weak bisimulation on streams, and thus an equivalence relation. For instance, $s_s \simeq ?\text{sensor}0^\omega$ (with $!\diamond$ unobservable). Again, the inductive definition of $t_1 \simeq t_2$ is Definition II.3 with reversed implication. Let $t \preceq t'$ when $t \simeq t''$ for some $t'' \leq t'$. Define $t_1 \preceq s_2$ similarly. Note that $s_1 \not\preceq s_2$ does *not* necessarily have a proof by induction on its derivation; this is the case when the observables of s_1 strictly prefix the observables in s_2 ; for some t , $s_1 \xrightarrow{t} \bullet^\omega$, $s_2 \xrightarrow{t} s'_2$, $s'_2 \xrightarrow{a} \bullet^\omega$ and $\bullet^\omega \not\rightarrow$, but $\bullet^\omega \not\rightarrow$ is an assertion about all elements in an infinite sequence. A proof of $t \preceq s$ is inductive, since t is finite. The following analog of (*=-and-ts*) is thus of interest.

$$s_1 \simeq s_2 \quad \text{iff} \quad \forall t. t \preceq s_1 \iff t \preceq s_2. \quad (\simeq \text{-and-} ts)$$

The other means we consider of comparing observables stipulates that there is never a disagreement in which observable comes next in two streams. That is, component-wise comparison of observables does not eventually yield two observables which differ. This holds e.g. when $s_1 \simeq s_2$ or when the observables in s_1 prefix the observables in s_2 .

Definition II.4. $\mathcal{R} \subseteq \mathbb{S} \times \mathbb{S}$ is a *feeble stream relation* iff

$$\begin{aligned} \forall s_1, s_2. s_1 \mathcal{R} s_2 &\implies s_1 \not\rightarrow \vee s_2 \not\rightarrow \vee \\ \exists a, s'_1, s'_2. s_1 &\xrightarrow{a} s'_1 \wedge s_2 \xrightarrow{a} s'_2 \wedge s'_1 \mathcal{R} s'_2 \end{aligned}$$

s_1 and s_2 are *feebly stream related*, $s_1 \approx s_2$, iff there exists a feeble stream relation \mathcal{R} such that $s_1 \mathcal{R} s_2$. \diamond

This is a coinductive definition of NC-similarity [12]. It is reflexive and symmetric, but not transitive, thus not an equivalence relation; $?sensor0.\bullet^\omega \approx \bullet^\omega$, $!zero0.\bullet^\omega \approx \bullet^\omega$, but $?sensor0.\bullet^\omega \not\approx !zero0.\bullet^\omega$ (indeed, for all s , $s \approx \bullet^\omega$). To contrast, $!zero0.\bullet^\omega \not\approx \bullet^\omega$. Again, the inductive definition of $t_1 \approx t_2$ is Definition II.4 with reversed implication. On traces, $t_1 \approx t_2$ iff $t_1 \preceq t_2$ or $t_2 \preceq t_1$. $s_1 \not\approx s_2$ has a proof by induction on its derivation, because eventuality of a component-wise difference can be represented as a trace t for which $t \preceq s_1$ but $t \not\preceq s_2$ (or vice versa). With $t \approx s$ defined in the obvious way ($t \approx s$ iff $t.\bullet^\omega \approx s$), $t \not\approx s$ also has an inductive proof.

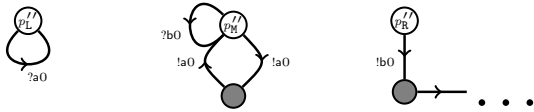
$$s_1 \approx s_2 \text{ iff } \forall t. t \not\approx s_1 \iff t \not\approx s_2. \quad (\approx\text{-and-ts})$$

Lemma II.5. $(=) \subsetneq (\approx) \subsetneq (\approx)$.

We close with a justification for focusing on stream semantics as a basis for comparing the behavior of processes. As noted earlier, actions are the *only* external interface to our processes. Thus, when reasoning about security, we model attacker knowledge obtained via *extensional* observations. Here, not allowing the attacker to observe the state of the process is natural. This makes branching-time equivalence (bisimulation on *processes*) too strict for our purposes, since it makes distinctions on the *internal* branching structure of processes [35]. We therefore opt for stream equivalence: a linear-time behavioral equivalence which subsumes trace equivalence

D. Interaction

The input-output behavior in the LTS_{IO} semantics of a program implies a particular model of interaction used by said program. Typically, secure information flow frameworks in the reactive, process algebra, and interactive program setting, adopt *synchronous* communication on channels as the model of communication [12], [16], [19], [22], [37], [40], [42], [43], [47]. Here, if we take the processes in these computation models and put them in interaction, then processes can *block* on both input and output. We argue that synchronous communication is not a good fit for general system composition, as the exhibited blocking behavior makes (compositional) reasoning about process behavior nontrivial. Progress of synchronously interactive programs is highly context sensitive, as nonwillingness of one component to receive can halt the progress of a sender component. For instance, consider the following LTS_{IO} .



Consider p_M'' in interaction with p_R'' . These components can synchronize $b0$, and under an inert environment, this synchronization *will* (eventually) occur. However, merely by adding p_L'' , a component which sends no messages, the eventuality of $b0$ is no longer guaranteed, as there is a (*fair*) scheduling which starves it: let (p_L'', p_M'') synchronize first, and then let (p_M'', p_R'') synchronize if they can; otherwise synchronize (p_L'', p_M'') twice. The same situation arises if we reverse the direction of b .

In practice, output blocking is typically avoided by buffering channels, making communication *asynchronous*. Then, p_R'' would put $!b0$ into a buffer without delay and move on with its computation, while p_M'' would subsequently read from the

buffer whenever p_M'' is ready to do so. However, this effect is also achieved by requiring that *there is always a receiver ready for each send*. This holds if each component in a composition is always ready to receive input, i.e., *input total* [27], [30]. Input totality simplifies reasoning about composed systems considerably [29], [57], as an input-total process cannot control its environment by not desiring certain input. Input totality abstracts from *how* asynchrony is achieved. Thus, our framework generalizes more concrete approaches e.g. input queues [52] and buffered channels [48]. To make fairness visible in an interaction stream, we ensure each component is always able to perform an action, regardless of which environment it is in. We get this by assuming that processes are *output productive*, i.e., always capable of producing output. This makes our processes similar to Input/Output Automata [27], in that processes are autonomous; a process can always make progress on its outputs. This is in contrast to asynchrony in process algebra (e.g. [23]), where output is only sent when a receiver is ready. Like in [27], we say a stream is *fair* if it contains an infinite number of outputs, i.e., is in the set

$$\mathbb{S}_F = \{s \in \mathbb{S} \mid \forall t \leq s. \exists t', o'. t.t'.o' \leq s\}.$$

Let $\mathbb{S}_F(p) = \mathbb{S}(p) \cap \mathbb{S}_F$. Since $!o$ models noninteraction, receivers of $!o$ should not react to it. Such receivers are *atemporal*. For further justification and merits of this model of concurrency, see Section VI.

Definition II.6 (Interactive LTS_{IO}). p is

- 1) *input total* iff $\forall t, p'. p \xrightarrow{t} p' \implies \forall i. p' \xrightarrow{i}$
- 2) *output productive* iff $\forall t, p'. p \xrightarrow{t} p' \implies \exists o. p' \xrightarrow{o}$
- 3) *atemporal* iff $\forall t, p'. p \xrightarrow{t} p' \implies \forall ?o, p''. p' \xrightarrow{?o} p'' \implies p' = p''$

p is *interactive* iff p satisfies 1), 2) and 3). \diamond

Putting two interactive LTS_{IO} in nonblocking interaction is now a simple matter of making all output of one process become the only input to the other (and vice versa). We write p under p' as $p' \models p$. p produces s under p' , $p' \models p \xrightarrow{s}$, iff, $p \xrightarrow{s}$ and $p' \xrightarrow{s-1}$. Similarly for traces. Function \cdot^{-1} reverses direction of messages, that is, $\epsilon^{-1} = \epsilon$, $(!m.t)^{-1} = ?m.t^{-1}$, and $(?m.t)^{-1} = !m.t^{-1}$, and similarly for streams. We refer to p' here as the *environment* of p , and we will study the behavior of p under different environments when reasoning about security of p . This is in stark contrast with previous work on security of LTS_{IO} [16], [37], [42] which considers (classes of) *strategies* as environments, i.e., functions of type $\mathbb{T} \rightarrow \mathbb{C} \rightarrow \mathcal{P}(\mathbb{V})$. One noteworthy feature which secure p -environments have over secure strategy-environments is p can force a secret input to occur before a public input, as input streams can in the reactive systems setting [12]; indeed, if p' and p are deterministic, and the interaction pattern is fixed, p' will behave like an input stream. Our framework thus unifies several previous frameworks for interactive program security.

We assume p is interactive throughout our development, unless stated otherwise. While these are strong restrictions to impose on an LTS_{IO} , any LTS_{IO} can be modeled as an interactive LTS_{IO} . For instance, for p which can discriminate

on which channel to receive on next, like the LTS_{IO} in [16], [37], [42], the *buffer* wrapper $B^?$ associates an input queue with each channel, which p can then receive on at its leisure. $B^?(p)$ is input total, for any p .

$$\frac{p \xrightarrow{o} p'}{B^?(t, p) \xrightarrow{o} B^?(t, p')} \quad \frac{p \xrightarrow{?cv} p' \quad \nexists(t''.?cv') \leq t}{B^?(t. ?cv. t', p) \xrightarrow{!o} B^?(t. t', p')} \\ \frac{}{B^?(p) = B^?(e, p)}$$

For programs which never discriminate on which channel to receive from, like the LTS_{IO} in [12], [43], [58], the *FIFO* wrapper $F^?$ buffers input and delivers it to p on demand, in FIFO order. For such p , $F^?(p)$ is input total.

$$\frac{p \xrightarrow{o} p'}{F^?(t, p) \xrightarrow{o} F^?(t, p')} \quad \frac{p \xrightarrow{i} p'}{F^?(i. t, p) \xrightarrow{!o} F^?(t, p')} \\ \frac{}{F^?(p) = F^?(e, p)}$$

Any p which is not output productive has the potential to block on input. The *wait* wrapper W empowers any such p with the ability to, instead of block, wait as an internal action. For any p , $W(p)$ is output productive.

$$\frac{\exists i. p \xrightarrow{i} \quad \nexists o. p \xrightarrow{o}}{W(p) \xrightarrow{!o} W(p)} \quad \frac{p \xrightarrow{a} p'}{W(p) \xrightarrow{a} W(p')}$$

At last, the *atemporal* wrapper T ignores any $?o$ actions. $T(p)$ is atemporal, for any p .

$$\frac{}{T(p) \xrightarrow{?o} T(p)} \quad \frac{p \xrightarrow{a} p' \quad a \neq ?o}{T(p) \xrightarrow{a} T(p')}$$

III. SECURITY OF INTERACTIVE SYSTEMS

Equipped with the tools from the previous section, we develop notions of information-flow security in our setting. We present two popular conditions of *possibilistic noninterference*: PSNI and PINI. While PSNI is well studied in our setting [16], [37], [42], we give the first formalization of PINI in a general interactive setting; PINI has thus far only been presented in settings with restricted forms of interaction [3], [12].

A. Observables

The observables of a process are its effects. We assume a lattice $(\mathcal{L}, \sqsubseteq)$, with \mathcal{L} ranged by ℓ , of security levels expressing levels of *confidentiality*. Each channel c is labeled with two security levels; $\pi(c)$ is the level of the *presence* of a message on c , and $\kappa(c)$ is the level of the *content*, or value, of a message on c . In examples, we frequently represent a channel by its security levels, writing $\kappa(c)^{\pi(c)}$ in place of c . A classic example is the lattice $\mathcal{L} = \{\text{L}, \text{H}\}$ (“low” (public) and “high” (secret)) and $\sqsubseteq = \{(\text{L}, \text{L}), (\text{L}, \text{H}), (\text{H}, \text{H})\}$. We let L , M and H denote L^{L} , H^{L} and H^{H} , respectively. We let \top resp. \perp denote the top resp. bottom element in the security lattice. Let $\$::= ? \mid !$ and define $\pi(\$cv) = \pi(c)$ and $\kappa(\$cv) = \kappa(c)$. Since timing-sensitive reasoning is beyond the scope of this paper, we set $\pi(\$) = \kappa(\$) = \top$. For termination-sensitive reasoning in this framework, set $\pi(\$*) = \kappa(\$*) = \perp$ and impose a restriction similar to atemporal for termination actions.

The security labels express *who* can observe *what*. An observer is associated a security level ℓ . An ℓ -observer is capable of observing the presence (resp. content) of messages on c if $\pi(c) \sqsubseteq \ell$ (resp. $\kappa(c) \sqsubseteq \ell$). Let $s|_{\ell}$ be the stream where, component-wise, each action has been replaced with what an ℓ -observer observes in the action:

$$(\$cv.s)|_{\ell} = \begin{cases} \bullet.s|_{\ell}, & \text{if } \pi(c) \not\sqsubseteq \ell \\ \$cd.s|_{\ell}, & \text{if } \pi(c) \sqsubseteq \ell \wedge \kappa(c) \not\sqsubseteq \ell \\ \$cv.s|_{\ell}, & \text{otherwise.} \end{cases}$$

For instance, $(?H0.?M1.!L2.!o^{\omega})|_{\text{L}} = \bullet.?Md.!L2.\bullet^{\omega}$. Here (and later), d is a constant. Let $s_1 \simeq_{\ell} s_2$ iff $s_1|_{\ell} \simeq s_2|_{\ell}$, and $s_1 \approx_{\ell} s_2$ iff $s_1|_{\ell} \approx s_2|_{\ell}$. Similarly for traces.

B. Noninterference

The idea behind noninterference is as follows. Assume an ℓ -observer observes all he is privileged to observe. A process is noninterfering if, based on ℓ -observables, the ℓ -observer learns nothing he is not privileged to learn, i.e., unobservable input does *not interfere* with observable behavior. Noninterfering processes are thereby not responsible for leaks (in our possibilistic setting, this means any difference in observable behavior must be attributable to nondeterministic choices).

To attribute a detected insecurity to the process under scrutiny, we study its behavior under secure environments. Typically, definitions of noninterference state that a process exhibits observably equivalent behavior, under any pair of non-interfering observably equivalent environments. In our setting, this leads to a circularity, since environments are processes. Previous work avoids this circularity by

- using simpler environments for which noninterference and observational equivalence are trivial [3], [19], [37],
- defining observational equivalence on processes, and non-interference as self-equivalence [12], [49], or
- defining noninterference as invariance of observable behavior to insertion/deletion of unobservable input [25].

We find that none of these approaches can be applied directly to our setting. Since compositionality is a main concern in this paper, we need environments to be part of the computation model, ruling out a). Since self-equivalences are bisimulation relations, they are branching-time equivalences, rejecting e.g. the following program p_{linear} , since it *can* enter the “else” branch, where it can leak information, even though it can also always take the “then” branch on x , where no information leaks. We therefore find b) too strict.

```
x = 0|1 ; out H x ; poll H h
if h = UNDEFINED then h = 0 end if
if x mod 2 = 1 then out L (0|1)
else out L (h mod 2) end if
```

Here, **poll** $c \ x$ is a nonblocking input interacting with a buffering context, similar to **if-recv** in [48]. If a c -input is waiting in the buffer, consume it. Otherwise, write UNDEFINED to x . Formally à la [42] (with $X = \text{UNDEFINED}$),

$$\frac{}{\langle \mu, \text{poll } c \ x \rangle \xrightarrow{i cv} \langle \mu[x \mapsto v], \text{skip} \rangle} \\ \frac{p \xrightarrow{i cv} p' \quad \nexists(t''.?cv') \leq t}{B^?(t. ?cv. t', p) \xrightarrow{!o} B^?(t. t', p')} \quad \frac{p \xrightarrow{i cX} p' \quad \nexists(t'.?cv) \leq t}{B^?(t, p) \xrightarrow{!o} B^?(t, p')}$$

Approaches based on c) are defined on traces, and have similar problems as b) in that they reject the above program; inserting $?H1$ immediately after $!o.!H0$ in $!o.!H0.!o^k.!L0$ makes the subsequent $!L0$ impossible (since $!H0$ fixes x to 0).

What we desire is a property which stipulates that a process *can* (by making the appropriate nondeterministic choices) *preserve the possibility* of a sequence of observables, under insertion of unobservable input during execution. To ensure that a stream is not only possible due to the presence of unobservable input, we require the above for streams which contain no unobservable input, i.e., streams over

$$\mathbb{A}_\ell = \bigcirc \cup \{?cv \mid \pi(c) \sqsubseteq \ell \wedge (\kappa(c) \not\sqsubseteq \ell \implies v = d)\}.$$

Since a process cannot leak information if denied the opportunity to produce output, we focus on fair runs of processes. We define the above as a coinductive predicate as follows.

Definition III.1. $p \mathcal{R}_\ell$ -preserves the possibility of s_0 after t through s , $\text{preserve}_{p,s_0}^{\ell,\mathcal{R}}(t,s)$, is the largest predicate satisfying each of the following.

- 1) $\forall o \leq s . \exists s' \in \mathbb{A}_\ell^\omega .$
 $s_0 \mathcal{R}_\ell t.o.s' \wedge p \xrightarrow{t.o.s'} \wedge \text{preserve}_{p,s_0}^{\ell,\mathcal{R}}(t.o,s')$
- 2) $\forall i \preceq_\ell s . \exists s' \in \mathbb{A}_\ell^\omega .$
 $s_0 \mathcal{R}_\ell t.i.s' \wedge p \xrightarrow{t.i.s'} \wedge \text{preserve}_{p,s_0}^{\ell,\mathcal{R}}(t.i,s')$
- 3) $\forall \tilde{i} \simeq_\ell s . \exists s' \in \mathbb{A}_\ell^\omega, \tilde{i} \leq \tilde{i}, o .$
 $s_0 \mathcal{R}_\ell t.\tilde{i}.o.s' \wedge p \xrightarrow{t.\tilde{i}.o.s'} \wedge \text{preserve}_{p,s_0}^{\ell,\mathcal{R}}(t.\tilde{i}.o,s') \quad \Diamond$

How is this definition invoked to state that p preserves the possibility of the observables in a fair stream s_0 for which $p \xrightarrow{s_0}$, under insertion of H input? If p is secure, then there is a fair $s \in \mathbb{A}_\ell^\omega$ for which $p \xrightarrow{s}$ and $s_0 \mathcal{R}_\ell s$. We then invoke the definition with $\text{preserve}_{p,s_0}^{\ell,\mathcal{R}}(\epsilon,s)$.

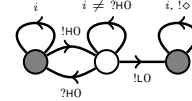
Definition III.2. p is \mathcal{R} -noninterfering iff $\forall \ell . \forall s \in \mathbb{S}_F(p) .$

$$\exists s' \in \mathbb{S}(p) \cap \mathbb{A}_\ell^\omega . s \mathcal{R}_\ell s' \wedge \text{preserve}_{p,s}^{\ell,\mathcal{R}}(\epsilon,s'). \quad \Diamond$$

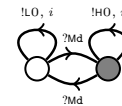
How does $\text{preserve}_{p,s_0}^{\ell,\mathcal{R}}(\epsilon,s)$ state that p preserves the possibility of the observables in s_0 ? 1) corresponds to the scenario where the environment feeds no input into p ; to match s_0 , p merely emits s , since we already have that $s_0 \mathcal{R}_\ell s$ ($s = o.s'$). 2) states what must hold when the environment provides H input at any time ($?cv \preceq_\ell \hat{s}$ holds for all \hat{s}, v and c for which $\pi(c) \sqsubseteq v$) and when the next observable action is an input; p must be able to choose a $s' \in \mathbb{A}_\ell^\omega$ matching the rest of s_0 . 3) states that when there are no more observable output remaining to be matched, p can be scheduled fairly while still preserving possibility of s_0 ; when p is being fed a stream of input, p can at some point cut into the (otherwise infinite) stream of input \tilde{i} after a (finite) trace of inputs $\tilde{i} \leq \tilde{i}$ and produce an unobservable output o .

When devising our properties, we encountered three challenging scenarios which our properties needed to deal with to guarantee eventuality of actions: i) *scheduling*, ii) *high interaction loops*, and iii) *high output starvation*. In i), since both p and its environment can be the producer behind the k th action in an interaction, p might only be able to choose a matching

s' if p produces the next message. However, the demand p places on its possibilistic scheduling may conflict with the demands of the environment. This is the case when both p and its environment only preserve confidentiality if they each are the producer behind the first message in the interaction. Having one yield fully to the demands of the other makes the security properties stipulate security for all schedulers, which implies security under scheduler *refinement* [59]. While interesting, we find this to be too conservative; we wish for our scheduler to be possibilistic (and therefore a source of nondeterminism which can conceal information leaks). In ii), future observables can be starved as a result of either unfortunate scheduling, or H input. To illustrate, consider e.g. the following process p_{loop} .



After receiving $?H0$, this process *insists* on outputting $H0$ before outputting $L0$. If the environment insists (to not leak) on outputting $H0$ before receiving $L0$, action $!L0$ is deferred, possibly indefinitely, as is possible when p_{loop} interacts with a variant of itself which first outputs $H0$. The process and its environment here are engaged in a *livelock*, exhibiting behavior reminiscent of a “hallway dance”, or more accurately, an Alphonse-Gaston routine (“After you”, followed by a back-and-forth “No, you first.” ad infinitum) [38]. In the presence of high interaction loops, security must ensure that p can eventually produce its next observable, regardless of what environment p is run under. Dually, the environment must tolerate receiving the next observable at any point in time, as different processes under it can demand high interaction loops terminated at different times. Thus 2) additionally guarantees possible eventual termination of high interaction loops between a secure p interacting with a secure environment (at the whim of the producer of the next observable), addressing i) and ii). Finally, 3) is designed to address iii), e.g., to ensure that after p has produced all its observables, it can still be scheduled fairly. Consider this example of high output starvation, p_{starve} .



Consider environments $p_1 = F^?(!Md^\omega)$ and $p_2 = F^?(!M0^\omega)$. Then $p_1 \models p_{\text{starve}}$ can match $?M0^\omega$ fairly, while $p_2 \models p_{\text{starve}}$ cannot; while $p_{\text{starve}} \xrightarrow{(?Md.!H0)^\omega}$, the first $i \preceq_\ell (?Md.!H0)^\omega$ fed to p_{starve} by p_2 is $i = ?M0$, making the matching stream with i as first action $?M0.(?Md.!H0)^\omega$; compared to $(?Md.!H0)^\omega$, the first output has been deferred. The only way for $p_2 \models p_{\text{starve}}$ to match $?M0^\omega$ is to not produce output at all, thus starving p_{starve} . 3) rejects programs of this nature.

Our “*preservation-based noninterference*” can be viewed as a hybrid of b) and c). In contrast to b), we do not require security to hold in all reachable states. In contrast to c), we do not require that each $s \in \mathbb{S}(p)$ satisfies insert/delete conditions; only that some $s' \mathcal{R}_\ell s$ does, i.e. that p *can* make the right nondeterministic choices to preserve the possibility of $\mathcal{R}_\ell s$.

Definition III.3. $p \in \text{PSNI}$ iff p is \simeq -noninterfering. \Diamond

Definition III.4. $p \in \text{PINI}$ iff p is \approx -noninterfering. \Diamond

Definition III.4 is the first definition of PINI in a general nondeterministic interactive setting. It differs from previous PINI formalizations [3], [4], [12] in that input to the process is not fixed before the process is run; rather, the environment is permitted to adapt its input based on prior process output. Our definition can be improved, however; consider p_{echo} , a process which outputs anything it receives in FIFO order (outputs $!o$ when its FIFO is empty), except when $?H0$ is received; then p_{echo} immediately becomes $F^?(!o^\omega)$. Consider environments $p_1 = !L0^\omega$ and $p_2 = !H0.!L0^\omega$. Then $p_1 \models p_{\text{echo}} \xrightarrow{(?L0.!L0)^\omega}$, which is \approx_ℓ -matched by $p_2 \models p_{\text{echo}} \xrightarrow{(?H0.!o)^\omega}$. However, the moment $p_2 \models p_{\text{echo}}$ inputs twice after $?H0$, \approx_ℓ -equivalence with $(?L0.!L0)^\omega$ is lost. Thus, in general, a p satisfying Definition III.4 might *have* to starve the environment to \approx_ℓ -match a stream. This is not unlike ID-security in [12], which allows a reactive system to ignore an observable input ready in the environment by diverging silently while reacting to a previous input. While we could adjust our definition of \approx_ℓ to cover the above scenario, the main reason we introduce PINI is to study how it behaves under composition, and the adjusted PINI would fail to compose in the same ways our PINI does.

PSNI is strictly stronger than PINI. This follows from Definition III.2, Lemma II.5 and this program which, wrapped in $W \circ B^? \circ Z$, is in $\text{PINI} \setminus \text{PSNI}$.

```
in H h
if (h mod 2) = 0 then out L 0 end if
```

Lemma III.5. $p \in \text{PSNI} \implies p \in \text{PINI}$

Pt. 2) in Definition III.1 enables a simple *proof technique* for guaranteeing eventuality of observable actions in an interaction: *schedule the producer of the next observable*. To see this in action, see the proofs of Theorems III.10 and IV.3.

C. Noninterference Under Environments

To facilitate evaluation of the relative merits of our preservation-based formalization of progress-(in)sensitive non-interference, and to demonstrate the generality of our framework, we give more conventional definitions of PSNI and PINI under environments à la [16], [37], [42].

Definition III.6. p_2 \mathcal{R}_ℓ -simulates p_1 , iff

$$\forall s_1 \in \mathbb{S}_F(p_1) \cdot \exists s_2 \in \mathbb{S}_F(p_2) \cdot s_1 \mathcal{R}_\ell s_2.$$

p_1, p_2 are \mathcal{R}_ℓ -equivalent, $p_1 \mathcal{R}_\ell p_2$, iff, $p_1 \mathcal{R}_\ell$ -simulates p_2 and $p_2 \mathcal{R}_\ell$ -simulates p_1 . \diamond

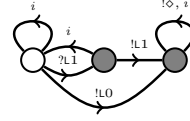
Definition III.7. p_2 \mathcal{R}_ℓ -E-simulates p_1 , iff

$$\begin{aligned} \forall p'_1, p'_2 \in \text{PSNI} \cdot p'_1 \simeq_\ell p'_2 \implies \\ \forall s_1 \in \mathbb{S}_F \cdot p'_1 \models p_1 \xrightarrow{s_1} \implies \\ \exists s_2 \in \mathbb{S}_F \cdot p'_2 \models p_2 \xrightarrow{s_2} \wedge s_1 \mathcal{R}_\ell s_2. \end{aligned} \quad \diamond$$

Definition III.8. $p \in \text{PSNI}_E$ iff $\forall \ell \cdot p \simeq_\ell$ -E-simulates p . \diamond

Definition III.9. $p \in \text{PINI}_E$ iff $\forall \ell \cdot p \approx_\ell$ -E-simulates p . \diamond

We use PSNI environments in the definition of PINI_E because otherwise, PINI_E becomes too conservative. To see this, say we defined PINI_E by relaxing \simeq_ℓ to \approx_ℓ and environment assumption PSNI to PINI in the definition of PSNI_E . Now consider the following process $p_{\text{PINI}_E} \in \text{PSNI}$.



Consider environments $p_1 = F^?(!L1.!o^\omega)$ and $p_2 = F^?(!o^\omega)$, for which we have $p_1, p_2 \in \text{PINI}$ and $p_1 \approx_L p_2$. However, while $p_1 \models p_{\text{PINI}_E} \xrightarrow{?L1.!L1.!o^\omega}$, $p_2 \models p_{\text{PINI}_E}$ cannot fairly either match these observables or remain silent; eventually, p_{PINI_E} performs $!L0$, and so, for any stream $s \in \mathbb{S}_F$ for which $p_2 \models p_{\text{PINI}_E} \xrightarrow{s}$, $s \not\approx_\ell ?L1.!L1.!o^\omega$. So, while replacing \simeq_ℓ in PSNI_E with \approx_ℓ weakens the security definition, relaxing the assumptions on the environments to PINI strengthens it immensely; the behavior of a process would need to be invariant to L input as well as H input to satisfy PINI_E .

For our preservation-based definitions to be useful on their own, they need to be stronger than the standard environment-based definitions; then we will know processes satisfying our preservation-based definitions are safe against all attacks which processes satisfying the environment-based definition are safe against. This is the case; the proof is in the appendix.

Theorem III.10. $p \in \text{PSNI} \implies p \in \text{PSNI}_E$

Theorem III.11. $p \in \text{PINI} \implies p \in \text{PINI}_E$

We suspect the reverse implication of these theorems to be false. To show that p satisfies the constraint imposed by Definition III.1 pt. 2) using assumption $p \in \text{PINI}_E$, it seems we need to propose an environment which outputs a different observable if it receives an unobservable first. However, such an environment is not PSNI (resp. PINI). These theorems give us a sense of assurance, however; if a property is too weak, say, $P = \{p \in \text{LTS}_{\text{IO}} \mid p \text{ is interactive}\}$, then $p \in P \not\Rightarrow p \in P_E$, since P_E places demands on p beyond $p \in P$.

For similar reasons as for Lemma III.12, PSNI_E is strictly stronger than PINI_E .

Lemma III.12. $p \in \text{PSNI}_E \implies p \in \text{PINI}_E$

Finally, we consider to which extent PSNI_E (resp. PINI_E) permits processes to starve the environment to preserve confidentiality. A process starving the environment is exerting control over the possibilistic scheduling of processes, which violates our desire for processes to be autonomous. Therefore, ideally, PSNI_E and PINI_E should reject processes which might *need* to starve the environment to preserve confidentiality. We say p *produces* s *fairly under* p' , $p' \models_F p \xrightarrow{s}$, iff $p' \models p \xrightarrow{s}$ and $s^{-1} \in \mathbb{S}_F$. Now let PSNI_{EF} (resp. PINI_{EF}) be defined as PSNI_E (resp. PINI_E) with “ \models ” replaced by “ \models_F ”. It turns out that PSNI_{EF} and PSNI_E are equivalent, and thus, that $p \in \text{PSNI}_E$ preserves interaction fairness when matching behaviors. However, as hinted at earlier, due to the way we formalized PINI_E , $p \in \text{PINI}_E$ may need to starve $p_2 \simeq_\ell p_1$ to match a behavior that $p_1 \models p$ can perform.

Theorem III.13. $p \in \text{PSNI}_{EF} \iff p \in \text{PSNI}_E$.

Theorem III.14. $p \in \text{PINI}_{EF} \not\iff p \in \text{PINI}_E$.

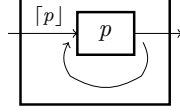


Fig. 1. Loop Combinator

D. Contrast to Trace-based Properties

Finally, to emphasize the novelty of our security properties, we demonstrate that they rule out classes of attacks which trace-based security properties, classically considered in work on compositionality in event systems [28], [32], [57], do not guarantee protection from. Consider the following program, which we refer to as the *extortionist*.

```
repeat poll H h until h ≠ UNDEFINED
out L 0
```

This program, turned into the process p_{extort} by buffering input, will repeatedly attempt a read on H in a nonblocking manner from its buffering context; if no H input is available in the context, then the program outputs L0.

We have $F^2(!H^\omega) \models p_{\text{extort}} \xrightarrow{(!\diamond. ?H0)^\omega}$. However, we have that $F^2(!\diamond) \models p_{\text{extort}}$ cannot match this behavior; thus, $p_{\text{extort}} \notin \text{PSNI}_E$, thus $p_{\text{extort}} \notin \text{PSNI}$. Alternatively, since \mathbb{A}_ℓ^ω has no fair stream matching this behavior, $p_{\text{extort}} \notin \text{PSNI}$.

However, p_{extort} does, for instance, satisfy *forward correctness* [25]. This is due to the fact that forward correctness is defined in terms of a trace semantics, and therefore cannot properly deal with the *definite (non)eventuality* of !L0. Pick a $t \in \mathbb{T}(p_{\text{extort}})$. If t has the L output, then for any $t = t'.t''$ and $?Hv.\hat{t}''$, there will be a \hat{t}'' with no H input for which $t \simeq_L t'.?Hv.\hat{t}''$. Similarly if a H input is deleted. If t does not have the L output, then regardless of whether a H input is inserted or deleted, setting $\hat{t}'' = \epsilon$ will \simeq_L -match t .

IV. COMPOSITIONAL SECURITY

To study how our security properties behave under composition, we present a minimal combinator language for building systems from parts. The core of this language is *complete* in the sense that arbitrary wirings between components can be constructed, yet *structured* in the sense that the possible routes that data can take in the composed system are clearly defined by the combinators used (as opposed to being partially defined by the (un)willingness of components to synchronize on certain channels at different times).

A. First Attempt

A minimal approach to enable two processes in a composed system to interact is to introduce a *loop* combinator $\llbracket \cdot \rrbracket$, illustrated in Figure 1. This is the approach taken in functional reactive programming [36], where loops and simple products (e.g. \oplus in Figure 3) enable modeling of arbitrary wirings.

$$p ::= \dots \mid \llbracket p \rrbracket$$

In $\llbracket p \rrbracket$, output from p is sent to the environment, and immediately, a copy of said output is sent into p as input. Any input to $\llbracket p \rrbracket$ is handed to p . The semantics of $\llbracket \cdot \rrbracket$ is as follows.

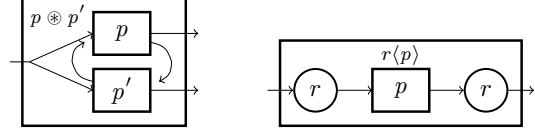


Fig. 2. Core Combinators

$$\frac{p \xrightarrow{o} p' \quad p' \xrightarrow{o^{-1}} p''}{\llbracket p \rrbracket \xrightarrow{o} \llbracket p'' \rrbracket} \llbracket \cdot \rrbracket^! \quad \frac{p \xrightarrow{i} p'}{\llbracket p \rrbracket \xrightarrow{i} \llbracket p' \rrbracket} \llbracket \cdot \rrbracket^?$$

Convenient as this combinator is, it enables a process to engage in a high interaction loop *with itself*. While it is possible to schedule $p_{\text{loop}} \in \text{PSNI}$ under an arbitrary environment s.t. !L0 eventually occurs, this is not the case for $\llbracket p_{\text{loop}} \rrbracket$; while $F^2(!L0.!\diamond^\omega) \models \llbracket p_{\text{loop}} \rrbracket \xrightarrow{?L0.!\diamond^\omega}$, $F^2(!H0.!\diamond^\omega) \not\models \llbracket p_{\text{loop}} \rrbracket$ cannot match this behavior; to do ?L0 without producing !L0, p_{loop} must consume ?H0. This sends p_{loop} to its leftmost state, where it returns *directly* after performing !H0, as the next action of p_{loop} is invariably !H0. While this poses no problems for PINI (since a high-interaction-looping process emits infinite silence), $\llbracket \cdot \rrbracket$ removes the degree of control a wrapped process needs to have for PSNI to be preserved under $\llbracket \cdot \rrbracket$.

Theorem IV.1. $p \in \text{PINI} \implies \llbracket p \rrbracket \in \text{PINI}$.

Theorem IV.2. $p \in \text{PSNI} \not\Rightarrow \llbracket p \rrbracket \in \text{PSNI}$.

B. Core Combinators

The issue with $\llbracket \cdot \rrbracket$ is that it can prevent the source of an output from making progress on its observable productions, by immediately following each output it makes with an input sent directly to the source. Therefore, if our combinators are to compose under PSNI, they need to at most enable output to reach any part of the system *except* its source, in one step of the whole system. We provide two combinators which we deem to be *core* combinators, sufficient to construct arbitrary such wirings: “and”, and “route”.

$$p ::= \dots \mid p \otimes p \mid r\langle p \rangle$$

These each take (possibly compound) interactive LTS_{IO} as parameter and yield a compound interactive LTS_{IO} . The structure that they impose is illustrated in Figure 2.

1) *and*: The *and* combinator produces a composite system from parts. An input to $p_1 \otimes p_2$ is sent to both p_1 and p_2 , while output from $p_1 \otimes p_2$ comes from exactly one of p_1 and p_2 , and is copied into the other as input, thus exhibiting *feedback*. This combinator is the *enabler* of communication, functioning as the “glue” with which we wire together larger systems from parts. Our \otimes combinator most closely resembles the (full, arbitrary, hook-up) binary composition typically used in event-based formalisms [28], or alternatively, a broadcasting variant [41] of parallel composition in process algebra [19]. The semantics of \otimes is as follows. It is clear that \otimes is associative and commutative.

$$\frac{p_L \xrightarrow{o} p'_L \quad p_R \xrightarrow{o^{-1}} p'_R}{p_L \otimes p_R \xrightarrow{o} p'_L \otimes p'_R} \otimes_L^! \quad \frac{p_R \xrightarrow{o} p'_R \quad p_L \xrightarrow{o^{-1}} p'_L}{p_L \otimes p_R \xrightarrow{o} p'_L \otimes p'_R} \otimes_R^! \\ \frac{p_L \xrightarrow{i} p'_L \quad p_R \xrightarrow{i} p'_R}{p_L \otimes p_R \xrightarrow{i} p'_L \otimes p'_R} \otimes^?$$

2) *router*: The *router* combinator $\langle \rangle$ wraps an LTS_{IO} in a context which routes messages. The routing is defined by a router function $r : \mathbb{A} \rightarrow \mathbb{A}$ satisfying $r(\mathbb{I}) \subseteq \mathbb{I}$, $r(\mathbb{O}) \subseteq \mathbb{O}$ and $r(\$ \diamond) = \$ \diamond$. Then in $r\langle p \rangle$, an output o leaving p is replaced with $r(o)$, and when an input i arrives at $r\langle p \rangle$, $r(i)$ is received by p . Whereas \otimes is an enabler of flows, where a composed system wires each output to each component (save the output source), $\langle \rangle$ can be used to *control* which underlying component receive which input, and to hide certain output from certain components. One example of the use of r is to map input actions on a particular channel (i.e., carrying high data) to $?\diamond$ to put the channel out of scope of the wrapped process. The semantics of $\langle \rangle$ are the following.

$$\frac{p \xrightarrow{r(i)} p'}{r\langle p \rangle \xrightarrow{i} r\langle p' \rangle} \langle \rangle^? \quad \frac{p \xrightarrow{o} p'}{r\langle p \rangle \xrightarrow{r(o)} r\langle p' \rangle} \langle \rangle^!$$

C. Point-to-Point Variant

Instead of our broadcasting message-passing semantics, one could alternatively opt for one which, instead of sending a message along both branches of a split arrow, sends it along exactly one of them. This yields a point-to-point message-passing semantics (which is still asynchronous), represented by the “xor” combinator.

$$p ::= \dots \mid p \boxtimes p$$

1) *xor*: The *xor* combinator \boxtimes is similar to parallel composition in point-to-point message-passing formalisms [19]. While the component wiring in \boxtimes is the same as for \otimes , the message-passing semantics differs notably: Input goes to exactly one component, and output goes exclusively to either the other component, or the environment. The semantics of \boxtimes is as follows. It is clear that \boxtimes is associative and commutative.

$$\begin{array}{c} \frac{p_L \xrightarrow{o} p'_L \quad p_R \xrightarrow{o^{-1}} p'_R}{p_L \boxtimes p_R \xrightarrow{i} p'_L \boxtimes p'_R} \boxtimes^{\text{I}_R} \quad \frac{p_L \xrightarrow{o} p'_L \quad p_R \xrightarrow{?o} p'_R}{p_L \boxtimes p_R \xrightarrow{o} p'_L \boxtimes p'_R} \boxtimes^{\text{I}_E} \\ \frac{p_R \xrightarrow{o} p'_R \quad p_L \xrightarrow{o^{-1}} p'_L}{p_R \boxtimes p_L \xrightarrow{i} p'_R \boxtimes p'_L} \boxtimes^{\text{I}_L} \quad \frac{p_R \xrightarrow{o} p'_R \quad p_L \xrightarrow{?o} p'_L}{p_R \boxtimes p_L \xrightarrow{o} p'_R \boxtimes p'_L} \boxtimes^{\text{I}_E} \\ \frac{p_L \xrightarrow{i} p'_L \quad p_R \xrightarrow{?o} p'_R}{p_L \boxtimes p_R \xrightarrow{i} p'_L \boxtimes p'_R} \boxtimes^? \quad \frac{p_R \xrightarrow{i} p'_R \quad p_L \xrightarrow{?o} p'_L}{p_R \boxtimes p_L \xrightarrow{i} p'_R \boxtimes p'_L} \boxtimes^? \end{array}$$

Using \boxtimes and $\langle \rangle$ as the combinator core would be viable in a synchronous concurrency model; there, input is only delivered to an *intended* receiver. However, in a setting where each component always waits for input on every channel, \boxtimes non-deterministically picks a component to receive the input. The input can therefore be sent along a branch in the composition which is not intended to receive the input (a constraint modeled using a router which maps it to $?\diamond$) and therefore never reach the intended target. We therefore find that \boxtimes is not a good fit in our framework – at least not as a *replacement* for \otimes . There are some merits to including \boxtimes in a language based on our combinator core, e.g. if, at the combinator level, one wishes to model nondeterministic dispatching of input to servers.

D. Compositionality

We now explore the compositionality of our security properties, to then give a *language of secure combinators* for

building secure systems from secure parts. While security properties are known to be fragile under composition [32], the proof technique arising from the design of our security properties yields positive results.

We consider compositionality of both \otimes and \boxtimes since they cannot be easily defined in terms of each other; \otimes and $\langle \rangle$ have no source of nondeterminism needed to model the nondeterministic behavior of \boxtimes (although this can be supplied by an LTS_{IO}), and no combination of \boxtimes and $\langle \rangle$ can guarantee eventual delivery between any pair of components like \otimes can.

1) *and*: We begin with the most important combinator, \otimes . It composes under PSNI.

Theorem IV.3. $p_L, p_R \in \text{PSNI} \implies p_L \otimes p_R \in \text{PSNI}$.

The proof of this is as follows. For any $p_L \otimes p_R \xrightarrow{s}$, we must show existence of a $p_L \otimes p_R \xrightarrow{s'}$ for which $s' \in \mathbb{A}_\ell^\omega$, $s \simeq_\ell s'$ and $\text{preserve}_{p_L \otimes p_R, s}(\epsilon, s')$. Since $p_L \otimes p_R \xrightarrow{s}$, there are some s_L, s_R for which $p_L \xrightarrow{s_L} p_R \xrightarrow{s_R}$ and $s_L \otimes s_R \xrightarrow{s}$. Indeed, for any n th action a_L, a_R and a in s_L, s_R and s respectively, if $a = i$ (environment input) for some i , then $a_L = i = a_R$, and if $a = o$ (component output), then either $a_L = o$ and $a_R = o^{-1}$, or vice versa. Since $p_L, p_R \in \text{PSNI}$, there exist $p_L \xrightarrow{s'_L}$ and $p_R \xrightarrow{s'_R}$ for which $s'_L, s'_R \in \mathbb{A}_\ell^\omega$, $s_L \simeq_\ell s'_L$, $s_R \simeq_\ell s'_R$, $\text{preserve}_{p_L, s_L}(\epsilon, s'_L)$ and $\text{preserve}_{p_R, s_R}(\epsilon, s'_R)$.

We obtain s' by “zipping” s'_L and s'_R in a manner guided by the observables in s_L and s_R (observables in both are the same as observables in s modulo direction) as follows:

Assume for $\hat{s}_L, \hat{s}_R \in \mathbb{A}_\ell^\omega$, t_L, t_R, t that $s_L \simeq_\ell t_L \cdot \hat{s}_L$, $s_R \simeq_\ell t_R \cdot \hat{s}_R$, $p_L \xrightarrow{t_L \cdot \hat{s}_L}$, $p_R \xrightarrow{t_R \cdot \hat{s}_R}$, $\text{preserve}_{p_L, s_L}(t_L, \hat{s}_L)$, $\text{preserve}_{p_R, s_R}(t_R, \hat{s}_R)$, $t \in \mathbb{A}_\ell^*$, $t \preceq_\ell s$, and $t_L \otimes t_R \xrightarrow{t}$.

We show existence of $\hat{s}'_L, \hat{s}'_R \in \mathbb{A}_\ell^\omega$, t'_L, t'_R, t' for which $s_L \simeq_\ell t'_L \cdot \hat{s}'_L$, $s_R \simeq_\ell t'_R \cdot \hat{s}'_R$, $p_L \xrightarrow{t'_L \cdot \hat{s}'_L}$, $p_R \xrightarrow{t'_R \cdot \hat{s}'_R}$, $\text{preserve}_{p_L, s_L}(t'_L, \hat{s}'_L)$, $\text{preserve}_{p_R, s_R}(t'_R, \hat{s}'_R)$, $t' \in \mathbb{A}_\ell^*$, $t' \preceq_\ell s$, $t'_L \otimes t'_R \xrightarrow{t'}$, $t_L < t'_L$, $t_R < t'_R$, $t < t'$, and $\hat{s}_L, \hat{s}_R \not\simeq_\ell \epsilon \implies t \not\simeq_\ell t'$. (*)

Assume $o_L \preceq_\ell \hat{s}_L$ for some $o_L \not\simeq_\ell \epsilon$ (proof for $o_R \preceq_\ell \hat{s}_R$ case obtained by swapping L and R). Then $\hat{s}_L = \bar{o}_L \cdot o_L \cdot \hat{s}_L''$ for some $\bar{o}_L \simeq_\ell \epsilon$ and \hat{s}_L'' . Through repeated application of Def. III.1 pt. 1), $s_L \simeq_\ell t_L \cdot \bar{o}_L \cdot o_L \cdot \hat{s}_L''$, $p_L \xrightarrow{t_L \cdot \bar{o}_L \cdot o_L \cdot \hat{s}_L''}$, and $\text{preserve}_{p_L, s_L}(t_L \cdot \bar{o}_L \cdot o_L, \hat{s}_L'')$. Through repeated application of Def. III.1 pt. 2), we have some \hat{s}_R'' for which $s_R \simeq_\ell t_R \cdot \bar{o}_L^{-1} \cdot o_L^{-1} \cdot \hat{s}_R''$, $p_R \xrightarrow{t_R \cdot \bar{o}_L^{-1} \cdot o_L^{-1} \cdot \hat{s}_R''}$, and $\text{preserve}_{p_R, s_R}(t_R \cdot \bar{o}_L^{-1} \cdot o_L^{-1}, \hat{s}_R'')$. Further, we have that $t_L \cdot \bar{o}_L \cdot o_L \otimes t_R \cdot \bar{o}_L^{-1} \cdot o_L^{-1} \xrightarrow{t \cdot \bar{o}_L \cdot o_L}$ and $p_L \otimes p_R \xrightarrow{t \cdot \bar{o}_L \cdot o_L}$. Set $\hat{s}'_L = \hat{s}_L''$, $\hat{s}'_R = \hat{s}_R''$, $t'_L = t_L \cdot \bar{o}_L \cdot o_L$, $t'_R = t_R \cdot \bar{o}_L^{-1} \cdot o_L^{-1}$, and $t' = t \cdot \bar{o}_L \cdot o_L$, and we get (*).

Assume $i \preceq_\ell \hat{s}_L$ and $i \preceq_\ell \hat{s}_R$ for some $i \not\simeq_\ell \epsilon$ and $i \in \mathbb{A}_\ell$. Through single application of Def. III.1 pt. 2), we have for some \hat{s}_L'' that $s_L \simeq_\ell t_L \cdot i \cdot \hat{s}_L''$, $p_L \xrightarrow{t_L \cdot i \cdot \hat{s}_L''}$, and $\text{preserve}_{p_L, s_L}(t_L \cdot i, \hat{s}_L'')$. Through single application of Def. III.1 pt. 2), we have for some \hat{s}_R'' that $s_R \simeq_\ell t_R \cdot i \cdot \hat{s}_R''$, $p_R \xrightarrow{t_R \cdot i \cdot \hat{s}_R''}$, and $\text{preserve}_{p_R, s_R}(t_R \cdot i, \hat{s}_R'')$. Further, we have that $t_L \cdot i \otimes t_R \cdot i \xrightarrow{t \cdot i}$ and $p_L \otimes p_R \xrightarrow{t \cdot i}$. Set $\hat{s}'_L = \hat{s}_L''$, $\hat{s}'_R = \hat{s}_R''$, $t'_L = t_L \cdot i$, $t'_R = t_R \cdot i$, and $t' = t \cdot i$, and we get (*).

Assume $\hat{s}_L \simeq_\ell \hat{s}_R \simeq_\ell \epsilon$ (equally valid proof obtained by swapping L and R in the following). Then $\hat{s}_L = o_L \cdot \hat{s}_L''$

for some $o_L \simeq_\ell \epsilon$ and \hat{s}_L'' . Through single application of Def. III.1 pt. 1), we have that $s_L \simeq_\ell t_R \cdot o_L \cdot \hat{s}_L''$, $p_L \xrightarrow{t_L \cdot o_L \cdot \hat{s}_L''}$, and preserve $_{p_L, s_L}(t_L \cdot o_L \cdot \hat{s}_L'')$. Through single application of Def. III.1 pt. 2), we have for some \hat{s}_R'' that $s_R \simeq_\ell t_R \cdot o_L^{-1} \cdot \hat{s}_R''$, $p_R \xrightarrow{t_R \cdot o_L^{-1} \cdot \hat{s}_R''}$, and preserve $_{p_R, s_R}(t_R \cdot o_L^{-1} \cdot \hat{s}_R'')$. Further, $t_L \cdot o_L \otimes t_R \cdot o_L \xrightarrow{t_L \cdot o_L}$ and $p_L \otimes p_R \xrightarrow{t_L \cdot o_L}$. Set $\hat{s}_L' = \hat{s}_L''$, $\hat{s}_R' = \hat{s}_R''$, $t_L' = t_L \cdot o_L$, $t_R' = t_R \cdot o_L^{-1}$, and $t' = t \cdot o_L$, and we get (*).

Recall $p_L, p_R \in \text{PSNI}$, $p_L \xrightarrow{s'_L}, p_R \xrightarrow{s'_R}, s'_L, s'_R \in \mathbb{A}_\ell^\omega$, $s_L \simeq_\ell s'_L$, $s_R \simeq_\ell s'_R$, preserve $_{p_L, s_L}(\epsilon, s'_L)$ and preserve $_{p_R, s_R}(\epsilon, s'_R)$. Now (*) gives us traces $t_0 < t_1 < \dots$ s.t. $p_L \otimes p_R \xrightarrow{t_j}$ and $t_j \in \mathbb{A}_\ell^*$ for all $j \geq 0$. Let s' be the fixed point of these traces. Then $p_L \otimes p_R \xrightarrow{s'}$ and $s' \in \mathbb{A}_\ell^\omega$, $s \simeq_\ell s'$.

To establish preserve $_{p_L \otimes p_R, s}(\epsilon, s')$, we proceed as follows. Assume for $\hat{s}_L, \hat{s}_R \in \mathbb{A}_\ell^\omega$, t_L, t_R, t that $s_L \simeq_\ell t_L \cdot \hat{s}_L$, $s_R \simeq_\ell t_R \cdot \hat{s}_R$, $p_L \xrightarrow{t_L \cdot \hat{s}_L}, p_R \xrightarrow{t_R \cdot \hat{s}_R}$, preserve $_{p_L, s_L}(t_L, \hat{s}_L)$, preserve $_{p_R, s_R}(t_R, \hat{s}_R)$, $\hat{s} \in \mathbb{A}_\ell^\omega$, $s \simeq_\ell t \cdot \hat{s}$, and $p_L \otimes p_R \xrightarrow{t \cdot \hat{s}}$.

To prove preserve $_{p_L \otimes p_R, s}(t, \hat{s})$, we must show for each of Def. III.1 pt. 1)-3) that there exist $s' \in \mathbb{A}_\ell^\omega$ and t' satisfying structural requirements imposed by the pt such that $s \simeq_\ell t' \cdot s'$ and $p_L \otimes p_R \xrightarrow{t' \cdot s'}$, and that there exist $\hat{s}_L', \hat{s}_R' \in \mathbb{A}_\ell^\omega$, t_L', t_R' for which $s_L \simeq_\ell t_L' \cdot \hat{s}_L'$, $s_R \simeq_\ell t_R' \cdot \hat{s}_R'$, $p_L \xrightarrow{t_L' \cdot \hat{s}_L'}, p_R \xrightarrow{t_R' \cdot \hat{s}_R'}$, preserve $_{p_L, s_L}(t_L', \hat{s}_L')$, preserve $_{p_R, s_R}(t_R', \hat{s}_R')$ and $t_L' \otimes t_R' \xrightarrow{t}$. (+).

For pt. 1), assume $o \leq \hat{s}$ for some o . Then either $o \leq \hat{s}_L$ or $o \leq \hat{s}_R$. Assume wlg that $o \leq \hat{s}_L$ (and that $o \leq \hat{s}$ came from p_L during construction of s'). Since preserve $_{p_L, s_L}(t_L, \hat{s}_L)$, we have through single application of Def. III.1 pt. 1) that for some \hat{s}_L' , $s_L \simeq_\ell t_L \cdot o \cdot \hat{s}_L'$, $p_L \xrightarrow{t_L \cdot o \cdot \hat{s}_L'}$, and preserve $_{p_L, s_L}(t_L \cdot o, \hat{s}_L')$. Since preserve $_{p_R, s_R}(t_R, \hat{s}_R)$, we get through single application of Def. III.1 pt. 2) some \hat{s}_R'' for which $s_R \simeq_\ell t_R \cdot o^{-1} \cdot \hat{s}_R''$, $p_R \xrightarrow{t_R \cdot o^{-1} \cdot \hat{s}_R''}$, and preserve $_{p_R, s_R}(t_R \cdot o^{-1}, \hat{s}_R'')$. Further, we have that $t_L \cdot o \otimes t_R \cdot o^{-1} \xrightarrow{t \cdot o}$ and $p_L \otimes p_R \xrightarrow{t \cdot o}$. Set $\hat{s}_L' = \hat{s}_L'$, $\hat{s}_R' = \hat{s}_R''$, $t_L' = t_L \cdot o$, $t_R' = t_R \cdot o^{-1}$, $t' = t \cdot o$, and use the above-described approach to obtain s' from these, and we get (+).

For pt. 2), assume $i \leq_\ell \hat{s}$ for some i . Then $i \leq_\ell \hat{s}_L$ and $i \leq_\ell \hat{s}_R$. By preserve $_{p_L, s_L}(t_L, \hat{s}_L)$ and preserve $_{p_R, s_R}(t_R, \hat{s}_R)$, we get through single application of Def. III.1 pt. 2) some \hat{s}_L', \hat{s}_R' for which $s_L \simeq_\ell t_L \cdot i \cdot \hat{s}_L'$, $s_R \simeq_\ell t_R \cdot i \cdot \hat{s}_R'$, $p_L \xrightarrow{t_L \cdot i \cdot \hat{s}_L'}, p_R \xrightarrow{t_R \cdot i \cdot \hat{s}_R'}$, preserve $_{p_L, s_L}(t_L \cdot i, \hat{s}_L')$, and preserve $_{p_R, s_R}(t_R \cdot i, \hat{s}_R')$. Set $\hat{s}_L' = \hat{s}_L'$, $\hat{s}_R' = \hat{s}_R'$, $t_L' = t_L \cdot i$, $t_R' = t_R \cdot i$, $t' = t \cdot i$, and use the above-described approach to obtain s' from these, and we get (+).

For pt. 3), assume $\bar{i} \simeq_\ell \hat{s}$ for some \bar{i} . By preserve $_{p_L, s_L}(t_L, \hat{s}_L)$ (the same argument with L and R swapped also holds), we get through single application of Def. III.1 pt. 3) some \bar{i} , o and \hat{s}_L' for which $\bar{i} \leq \bar{i}$, $s_L \simeq_\ell t_L \cdot \bar{i} \cdot o \cdot \hat{s}_L'$, $p_L \xrightarrow{t_L \cdot \bar{i} \cdot o \cdot \hat{s}_L'}$ and preserve $_{p_L, s_L}(t_L \cdot \bar{i} \cdot o, \hat{s}_L')$. By repeated application of pt. 2) for each i in \bar{i} and for o^{-1} , we have for some \hat{s}_R' that $s_R \simeq_\ell t_R \cdot \bar{i} \cdot o^{-1} \cdot \hat{s}_R'$, $p_R \xrightarrow{t_R \cdot \bar{i} \cdot o^{-1} \cdot \hat{s}_R'}$ and preserve $_{p_R, s_R}(t_R \cdot \bar{i} \cdot o^{-1}, \hat{s}_R')$. Set $\hat{s}_L' = \hat{s}_L'$, $\hat{s}_R' = \hat{s}_R'$, $t_L' = t_L \cdot \bar{i} \cdot o$, $t_R' = t_R \cdot \bar{i} \cdot o^{-1}$, $t' = t \cdot \bar{i} \cdot o$, and use the above-described approach to obtain s' from these, and we get (+).

Thus preserve $_{p_L \otimes p_R, s}(\epsilon, s')$, which completes this proof.

Furthermore, it turns out that PINI composes under \otimes , if we e.g. change the way \hat{s}_L' and \hat{s}_R' are zipped to form s' such that if one runs out of observables, say, \hat{s}_L' , then the rest of s' is set to the rest of \hat{s}_L' .

Corollary IV.4. $p_L, p_R \in \text{PINI} \implies p_L \otimes p_R \in \text{PINI}$.

2) *xor*: The proof that PSNI and PINI compose under \boxtimes is similar to the above “zip-and-preserve” proof of compositionality of \otimes . Since environment input does not enter both components, and since output from a component does not both go to the environment and the other component, the zipping procedure consults s in addition to \hat{s}_L and \hat{s}_R since s tells us where observables in s_L and s_R came from and went to.

Corollary IV.5. $p_L, p_R \in \text{PSNI} \implies p_L \boxtimes p_R \in \text{PSNI}$.

Corollary IV.6. $p_L, p_R \in \text{PINI} \implies p_L \boxtimes p_R \in \text{PINI}$.

3) *route*: Since a router can route any message to any channel, and change values in messages, a router has the capacity to reveal the presence of H-presences message or values in H-content messages. However, as long as a router function never moves information down in the security lattice, wrapping a secure process in it yields a secure process.

Definition IV.7. r is ℓ -secure iff $\forall c, c_1, v, v_1$.

$$r(\$cv) = \$c_1 v_1 \wedge \kappa(c) \not\sqsubseteq \ell \implies$$

- $\pi(c) \sqsubseteq \ell \implies \forall v', c_2, v_2. r(\$cv') = \$c_2 v_2 \implies c_1 = c_2$
- $\pi(c) \not\sqsubseteq \ell \implies \pi(c_1) \not\sqsubseteq \ell$.

We say r is *secure* if it is ℓ -secure for all ℓ . \diamond

Compositionality of $\langle \rangle$ is immediate from Definition III.1.

Corollary IV.8. $p \in \text{PSNI} \implies r\langle p \rangle \in \text{PSNI}$ for secure r .

Corollary IV.9. $p \in \text{PINI} \implies r\langle p \rangle \in \text{PINI}$ for secure r .

E. Fairness

As we have discussed, autonomy, and therefore fairness, is a key feature of interactive LTS_{IO} . However, the proof we just saw does not rule out the possibility of starvation. For instance, in the zipper given in the proof of \otimes , when \hat{s}_L has an infinite number of observables and \hat{s}_R has no observable output, the zipper can ignore the remainder of \hat{s}_R . Also, the PINI zipper ignores the remainder of \hat{s}_R when \hat{s}_L has no more observables. The central question here is whether our security properties *require* starvation of components to remain compositional. This prompts us to study *fair* combinators. What we find is that whereas PSNI composes freely assuming fairness, PINI relies *fundamentally* on *lack of fairness* to be compositional for even simple product compositions.

1) *Fair Composition*: A fair combinator permits only fair behaviors, i.e., ones which do not starve components, always allowing each to eventually make progress on its outputs.

Definition IV.10. For $p_L \otimes p_R \xrightarrow{s}$, s is \otimes -fair, iff, $\forall t \leq s$.

$$\exists t_L, t_R, o_L, o_R, s_L, s_R, p_{LL}, p_{RL}, p_{LR}, p_{RR}, p'_{LL}, p'_{LR}, p'_{RL}, p'_{RR} \cdot$$

$$s = t \cdot t_L \cdot o_L \cdot s_L \wedge s = t \cdot t_R \cdot o_R \cdot s_R \wedge$$

$$p_L \otimes p_R \xrightarrow{t \cdot t_L} p_{LL} \otimes p_{RL} \xrightarrow{o_L} p'_{LL} \otimes p'_{RL} \xrightarrow{s_L} \wedge$$

$$p_L \otimes p_R \xrightarrow{t \cdot t_R} p_{LR} \otimes p_{RR} \xrightarrow{o_R} p'_{LR} \otimes p'_{RR} \xrightarrow{s_R} \wedge$$

$$p_{LL} \xrightarrow{o_L} p'_{LL} \wedge p_{RL} \xrightarrow{o_L^{-1}} p'_{RL} \wedge$$

$$p_{RR} \xrightarrow{o_R} p'_{RR} \wedge p_{LR} \xrightarrow{o_L^{-1}} p'_{LR}.$$

$p_L \otimes_F p_R \xrightarrow{s} \text{iff } p_L \otimes p_R \xrightarrow{s} \text{ and } s \text{ is } \otimes\text{-fair.}$ \diamond

We let \boxtimes_F be defined in a similar manner.

2) *PSNI Composes Fairly*: Modifying the zip-and-preserve proof above for the compositionality of PSNI under \otimes_F and \boxtimes_F is easy; when \hat{s}_R runs out of observable output, we zip in such a way that \hat{s}' takes turns in pulling an output from \hat{s}_R and \hat{s}_L into t , irrespective of when and how many input appears before the outputs. We therefore have the following.

Corollary IV.11. $p_L, p_R \in \text{PSNI} \implies p_L \otimes_F p_R \in \text{PSNI}.$

Corollary IV.12. $p_L, p_R \in \text{PSNI} \implies p_L \boxtimes_F p_R \in \text{PSNI}.$

3) *PINI Composes Unfairly*: However, the same cannot be said for PINI; it fails to be preserved under even simple fair products. The way in which PINI fails to compose is *not* due to the simplification we made of the definition of PINI discussed in Section III-B. We therefore maintain that, in the interactive setting, PINI relies *fundamentally* on unfairness to be preserved under composition, making it a *poor* target property for reasoning about security of autonomous processes.

Theorem IV.13. $p_L, p_R \in \text{PINI} \not\Rightarrow p_L \otimes_F p_R \in \text{PINI}.$

To prove this, we give two programs which satisfy PINI which fair composition fails to be PINI. It illustrates how a progress-difference in one component can translate into an *explicit flow* under composition. Consider this program p_A ,

```
in M h
if h mod 2 = 0 then out L 0 end if
```

and the following program p_B .

```
out L 1
```

Wrapped in $W \circ B^? \circ Z$, both satisfy PINI. In fact, p_B satisfies PSNI. Now consider environments $p_1 = F^?(!M0.!o^\omega)$ and $p_2 = F^?(!M1.!o^\omega)$. Then we have that $p_1 \models p_A \otimes_F p_B$ can match $?M0.!L0.!L1$. However, $p_2 \models p_A \otimes_F p_B$ cannot match this behavior; p_A produces no observables, and the composition cannot postpone the observable p_B wishes to perform indefinitely, so at best, $p_1 \models p_A \otimes_F p_B$ can match $?M1.!L1$, which is not \approx_ℓ -equivalent to $?M0.!L0.!L1$. Thus $p_A \otimes_F p_B \notin \text{PINI}_E$. and thus, $p_A \otimes_F p_B \notin \text{PINI}.$

This same counterexample shows PINI also fails to compose under \boxtimes_F .

Corollary IV.14. $p_L, p_R \in \text{PINI} \not\Rightarrow p_L \boxtimes_F p_R \in \text{PINI}.$

While PINI may be justifiable if it composes under simpler combinators, it turns out that PINI fails to compose fairly *even* under products and cascades, as we will see in Section V-B. Finally, $p_B \in \text{PSNI}$. This means that even if a process is the *only* PINI process in a composition with PSNI processes, it cannot be guaranteed that the composition *even* satisfies PINI. The only way we can be sure of this in general is if each PINI process operates in a part of the security lattice *disjoint* from where all other processes operate, as then, varied presence of output by PINI components will not interfere with the behavior of the other components.

For these reasons, we deem PINI unfit for reasoning about security of autonomous processes.

V. LANGUAGE OF SECURE COMBINATORS

To showcase the generality of our combinator core and compositionality results, we give an rich language of combinators with which to build secure systems from secure parts. Since the combinators are implemented in terms of core combinators, they all compose under PSNI and PINI, and compose fairly under PSNI.

A. Derived Combinators

Figure 3 contains the full set of binary combinators which, for each component, has a path from input, through it, to output. Some of them appear regularly in literature on compositionality of security properties, e.g. product, (relaxed) cascade and feedback [28], [32], [56]. We show how these, and other, combinators can be implemented in terms of our core combinators. For comparison, we have placed their operational semantics in the appendix.

1) *Relaxed Cascade Feedback*: This combinator, denoted \otimes , behaves like \otimes , except that input to the composition is only delivered to the left component in the composition. The combinator can therefore be seen as a *relaxed* relaxed cascade. Using our combinators, we can implement \otimes as

$$p_L \otimes p_R = r_E \langle r_L \langle p_L \rangle \otimes r_R \langle p_R \rangle \rangle,$$

where

$$\begin{array}{lll} r_E(i) = i_E & r_L(o) = o_L & r_R(o) = o_R \\ r_E(o_L) = o & r_L(i_E) = i & r_R(i_E) = ?\diamond \\ r_E(o_R) = o & r_L(i_R) = i & r_R(i_L) = i. \end{array}$$

Here, a_X is a which message is labeled X (e.g. by partitioning \mathbb{C} or \mathbb{V}). The label expresses who is the producer behind a ; E is the environment, L is p_L and R is p_R . Thus, for instance, $r_R(i_E) = ?\diamond$ expresses that p_R should not receive a message that originated from the environment.

2) *Relaxed Cascade*: This combinator, denoted \odot , is a relaxation of cascade (i.e., sequential) composition often considered in work in compositionality. Like in a cascade, input to a relaxed cascade enters only the first component, and output from the second component is sent only to the environment. However, output from the first component is replicated and sent both to the environment and the second component. Using our core, we can implement \odot as

$$p_L \odot p_R = r_E \langle r_L \langle p_L \rangle \otimes r_R \langle p_R \rangle \rangle,$$

where

$$\begin{array}{lll} r_E(i) = i_E & r_L(o) = o_L & r_R(o) = o_R \\ r_E(o_L) = o & r_L(i_E) = i & r_R(i_E) = ?\diamond \\ r_E(o_R) = o & r_L(i_R) = ?\diamond & r_R(i_L) = i. \end{array}$$

3) *Cascade*: Also known as sequential composition, combinator \ominus is a basic combinator typically seen as a primitive in various combinator formalisms (e.g. [36]). Input entering a cascade enters the first component only, output from the first component enters the second component only, and output from the second component becomes the output of the cascade. Using our core, we can implement \ominus as

$$p_L \ominus p_R = r_E \langle r_L \langle p_L \rangle \otimes r_R \langle p_R \rangle \rangle,$$

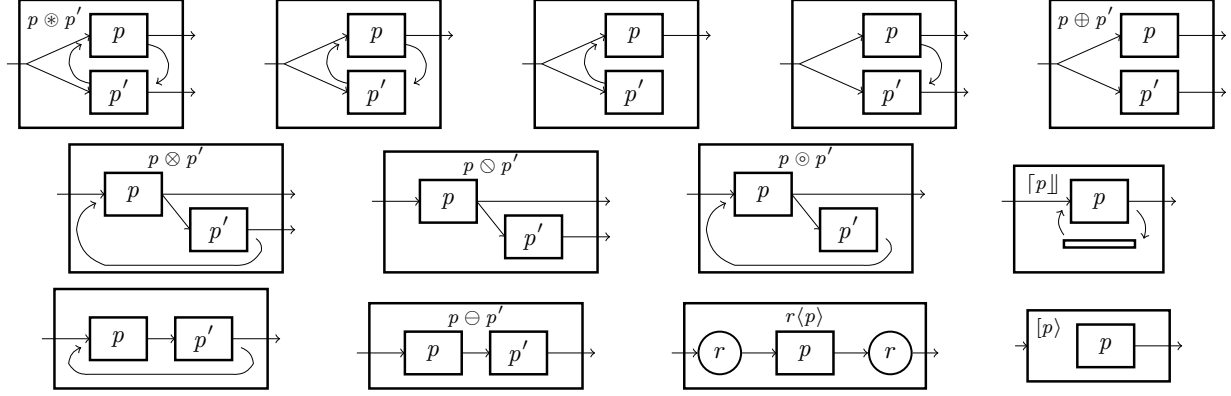


Fig. 3. Secure Combinators

where

$$\begin{array}{lll} r_E(i) = i_E & r_L(o) = o_L & r_R(o) = o_R \\ r_E(o_L) = !\diamond & r_L(i_E) = i & r_R(i_E) = ?\diamond \\ r_E(o_R) = o & r_L(i_R) = ?\diamond & r_R(i_L) = i. \end{array}$$

4) *Product*: This combinator, denoted \oplus , behaves like \otimes without feedback (or alternatively, like parallel composition without sharing [8]). Such *product* compositions are frequently considered in work on compositionality (e.g. [28], [42]) and taken as a primitive in combinator formalisms (e.g. [36]). Using our combinators, we can implement \oplus as

$$p_L \oplus p_R = r_E \langle r_L \langle p_L \rangle \otimes r_R \langle p_R \rangle \rangle,$$

where

$$\begin{array}{lll} r_E(i) = i_E & r_L(o) = o_L & r_R(o) = o_R \\ r_E(o_L) = o & r_L(i_E) = i & r_R(i_E) = i \\ r_E(o_R) = o & r_L(i_R) = ?\diamond & r_R(i_L) = ?\diamond. \end{array}$$

5) *Feedback*: A specialization of \otimes , this combinator, denoted \odot , isolates the right-component, making it interact only with the left component. This is useful for modeling interaction with a closed system. We implement \odot as

$$p_L \odot p_R = r_E \langle r_L \langle p_L \rangle \otimes r_R \langle p_R \rangle \rangle,$$

where

$$\begin{array}{lll} r_E(i) = i_E & r_L(o) = o_L & r_R(o) = o_R \\ r_E(o_L) = o & r_L(i_E) = i & r_R(i_E) = ?\diamond \\ r_E(o_R) = !\diamond & r_L(i_R) = i & r_R(i_L) = i. \end{array}$$

6) *Buffered Loop*: Placing a FIFO as the right component of \odot yields a buffered loop combinator, denoted $\llbracket \cdot \rrbracket$. To see this, let $p_F = W(F(\epsilon))$, where F is defined by $F(\bar{o}) \xrightarrow{i} F(\bar{o}.i^{-1})$ and $F(o.\bar{o}) \xrightarrow{o} F(\bar{o})$. Using this process, we can implement the buffered loop combinator as follows.

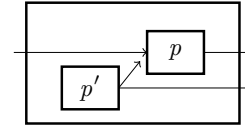
$$\llbracket p \rrbracket = p \odot p_F.$$

This loop combinator avoids the compositionality issues $\llbracket \cdot \rrbracket$ has by storing loop messages in the FIFO p_F (which p can consume from at its leisure), instead of jamming them directly into p . This ensures that p can make progress on its outputs.

7) *Generator*: Any interactive LTS_{IO} can be used as a source of information / input by never delivering input to it. We define the *generator* combinator $\langle \cdot \rangle$ as

$$\langle p \rangle = r_{\text{drop}} \langle p \rangle,$$

where $r_{\text{drop}}(i) = ?\diamond$ and $r_{\text{drop}}(o) = o$. When used in conjunction with the binary combinators in Figure 3, we obtain *three new combinators* for introducing information into a system. These are $p \oplus \langle p' \rangle$ (p' streams to the environment), $p \odot \langle p' \rangle$ (p' streams to p) and $p \otimes \langle p' \rangle$ (p' streams to both), the last of which can be illustrated as follows.



While for $r_{\text{mute}}(i) = i$ and $r_{\text{mute}}(o) = !\diamond$, one might consider including $r_{\text{mute}} \langle \cdot \rangle$ as a combinator, we find that for any binary combinator \odot , $p \odot r_{\text{mute}} \langle p' \rangle$ behaves either as p or as $r_{\text{mute}} \langle p' \rangle$ (and $r_{\text{mute}} \langle p' \rangle$ is semantically equivalent to $F^?(!\diamond^\omega)$).

B. Compositionality

Since all of the combinators presented in Figure 3 are specializations of \otimes and $\langle \cdot \rangle$, we have for all of them, and their counterparts based on \boxtimes instead of \otimes , the following compositionality properties.

Corollary V.1. For each binary combinator \odot in Figure 3,

$$p_L, p_R \in \text{PINI} \implies p_L \odot p_R \in \text{PINI},$$

$$p_L, p_R \in \text{PSNI} \implies p_L \odot p_R \in \text{PSNI},$$

$$p_L, p_R \in \text{PSNI} \implies p_L \odot_F p_R \in \text{PSNI}.$$

For each corresponding operator \boxdot based on \boxtimes , the same holds.

For $\llbracket \cdot \rrbracket$, and its counterpart based on \boxtimes ,

$$p \in \text{PINI} \implies \llbracket p \rrbracket \in \text{PINI},$$

$$p \in \text{PSNI} \implies \llbracket p \rrbracket \in \text{PSNI},$$

$$p \in \text{PSNI} \implies \llbracket p \rrbracket_F \in \text{PSNI}.$$

For $[\cdot]$,

$$p \in \text{PINI} \implies [p] \in \text{PINI},$$

$$p \in \text{PSNI} \implies [p] \in \text{PSNI}.$$

While the above corollary implies that any composite system consisting of PSNI components and combinators in Figure 3 satisfies PSNI regardless of fairness, the same cannot be said about PINI. When fairness is assumed, PINI fails to compose for even simple combinators; the counterexample in the proof of Theorem IV.13 also applies to products \oplus_F and \boxplus_F , and we have pointed out that PINI does not behave well under cascade \ominus_F (see our rationale for considering PSNI environments when defining PINI_E in Section III-C).

Corollary V.2. For each $\triangle \in \{\oplus, \boxplus, \ominus\}$,

$$p_L, p_R \in \text{PINI} \not\Rightarrow p_L \triangle p_R \in \text{PINI}.$$

C. Building Secure Systems From Parts

By the above result, we now have a rich toolset for building secure wholes from secure parts. Large systems are often developed in a modular manner, in different programming languages, and once deployed, run distributed over a network. Our combinators facilitate *end-to-end security*, and a divide-and-conquer approach to building large secure systems. Parts can be proven secure by use of language-based or language-independent enforcement mechanisms that target our security properties. Once the parts are proven secure, we have that the whole, assembled using our combinators, is secure. Combinators can be used to model the *network topology* (how the parts are “hard-wired” or nested), while routers can express data-dependent traffic routing in the network.

The combinators and our system model can also be used to formalize the concurrency semantics in a programming language, like Erlang. Furthermore, by proposing suitable primitive interactive LTS_{IO} , our combinators can be a *programming language* for writing asynchronous message-passing systems. One could, say, replace $\langle \rangle$ with \ominus as a primitive, if the routing delay this introduces at the semantics level is not problematic. However, for such a language to be expressive, combinators for programmatically changing the wiring of components (e.g. switches in functional reactive programming [36] and name-passing in process algebra [22]) should be introduced.

VI. RELATED WORK

To aid in understanding the relative merits of the various models of interaction we are about to discuss, we classify LTS_{IO} based on the interaction behavior they exhibit.

Definition VI.1 (LTS_{IO} classification). p is

- 1) *input value neutral* iff $\forall t, p'. p \xrightarrow{t} p' \implies (\exists ?cv. p' \xrightarrow{?cv} \cdot) \implies \forall v'. p' \xrightarrow{?cv'} \cdot$.
- 2) *input neutral* iff $\forall t, p'. p \xrightarrow{t} p' \implies (\exists i. p' \xrightarrow{i} \cdot) \implies \forall i'. p' \xrightarrow{i'} \cdot$.
- 3) *reactive* iff $\forall t, p'. p \xrightarrow{t} p' \implies (\exists i. p' \xrightarrow{i} \cdot) \implies (\nexists i', i''. p' \xrightarrow{i'.i''} \cdot) \wedge (\nexists o. p' \xrightarrow{o} \cdot)$.
- 4) *productive* iff $\forall t, p'. p \xrightarrow{t} p' \implies \exists a. p' \xrightarrow{a} \cdot$.
- 5) *internally deterministic* iff $\forall t, p'. p \xrightarrow{t} p' \implies$

$$\begin{aligned} & \forall a, p'_1, p'_2. p' \xrightarrow{a} p'_1 \wedge p' \xrightarrow{a} p'_2 \implies p'_1 = p'_2 \\ 6) \text{ input deterministic} & \quad \text{iff } \forall t, p'. p \xrightarrow{t} p' \implies \\ & \quad \forall i_1, i_2. p' \xrightarrow{i_1} \cdot \wedge p' \xrightarrow{i_2} \cdot \implies \exists c. i_1, i_2 \in \{?cv \mid v \in \mathbb{V}\} \\ 7) \text{ output deterministic} & \quad \text{iff } \forall t, p'. p \xrightarrow{t} p' \implies \\ & \quad \forall o_1, o_2. p' \xrightarrow{o_1} \cdot \wedge p' \xrightarrow{o_2} \cdot \implies o_1 = o_2 \quad \diamond \end{aligned}$$

Event systems [20], [28], [29], [31], [54], [57] are essentially LTS_{IO} with no restrictions applied. Trace semantics is used as the underlying notion of behavioral equivalence. Compositionality of information flow properties, under a binary operator which implicitly wires matching communication channels internally, has been thoroughly studied in this setting in theories developed for reasoning about compositionality [28], [32], [57]. McLean [32], Zakinthinos and Lee [56] showed that *noninference*, *separability* and *perfect security* are all compositional, and McLean further showed that *generalized noninference* and *generalized noninterference* compose under product. Johnson and Thayer showed that *forward correctability* is fully compositional [25]. McCullough first demonstrated that generalized noninterference is *not* fully compositional [29]. However, Zakinthinos and Lee have shown that generalized noninterference composes under certain conditions: under a relaxed cascade [55], if every feedback loop involves at least three components [56], or if a delay component is inserted into the feedback of high events [55]. Mantel [28] derived all the above results save the last two using his modular assembly kit for security properties (MAKS). He also derived several new conditional compositionality results, and showed that a *weakened forward correctability* is compositional. Our PSNI composes under routing, product, and under cascade and feedback provided a FIFO is placed between components. Our combinators offer a structured way of composing secure systems from parts; no wiring is implicit, and the possible routes that data can take are clearly defined by structure and routers. Our properties use stream semantics as the underlying behavioral equivalence, which we have argued and demonstrated, makes more, desirable distinctions, enabling us to reject the “extortionist” program given in Section III-D.

Process calculi for security [19], [22], [23], [40], [46], [47] have LTS_{IO} as their underlying semantics. They study the use of algebraic properties of concrete concurrency constructs. We are more abstract, providing results for LTS_{IO} directly. We assume input totality in our framework, which induces a concurrency semantics free of output blocking, similar to mailboxes in the Actor model [1] (implemented in Erlang), message queues in JavaScript, and buffered I/O in most programming languages. Assuming input totality simplifies system composition considerably [57]. The parallel composition operator also implicitly wires channels. Bisimulation on processes is typically provided as the primary tool for behavioral reasoning. Since bisimulation is a branching-time, it makes undesired distinctions, which our behavioral equivalence avoids.

Reactive systems [12], [43], [58] are, in the sense of Definition VI.1, reactive, input neutral and productive LTS_{IO} . Bohannon et al. [12] present and contrast four stream-based possibilistic noninterference definitions, emphasizing CP-security and ID-security, and give a type-based enforcement of ID-security. While ID-security and CP-security do not exclude nondeterministic programs, ID- and CP-security are very restrictive for nondeterministic programs, rejecting programs

which conceal information using nondeterministic choice, and therefore essentially becoming as strict as low observational determinism [59] or security under refinement [37]. Our definition of PINI can therefore be perceived as a more faithful generalization of PINI from [3] to nondeterministic systems, or as a generalization of ID-security to nondeterministic systems with intermediate input. We have shown that PINI does not compose under fair schedulers. The counterexamples can easily be expressed in the language of [12]; thus, ID-security does not compose fairly. While the *transducer* impression of reactive systems suggests easy composition, reactive systems are not input total; non-willingness of a component to receive can halt progress of another component that wishes to send.

Our security framework most closely resembles the LTS_{IO} - and strategy-based frameworks for possibilistic noninterference [16], [37], [42], [44]. O’Neill et al. [37] present a single-threaded programming language which LTS_{IO} is input neutral, internally- and output-deterministic. The target property is strategy-based PSNI, originally inspired by *nondeducibility on strategies* by Wittbold and Johnson [54]. Extending the language with nondeterministic choice (making their LTS_{IO} no longer deterministic), they modify PSNI to require non-interference under *refinement* (arbitrary determinization of all nondeterministic choices prior to execution). Clark and Hunt [16] instead give a *possibilistic* version of PSNI, show that it is sufficient to guarantee security under deterministic strategies to prove that a program is PSNI, and that stream strategies are sufficient if the program is internally-, input- and output-deterministic, a result used by [12]. In both of these settings, strategies are *total*; strategies are always willing to receive, and regardless of when and on which channel a program blocks on, the strategy has input available on said channel. While this may be a good fit when strategies model local memory, as demonstrated by Rafnsson et al. [42], this total strategies assumption ignores classes of realizable attacks which encode secrets in the varied presence of messages in a concurrent setting. This motivates distinguishing between sensitivity of message presence and content, as we do in the present paper, which none of the work discussed so far does, save for [42]–[44]. This idea can be traced back to Sabelfeld and Mantel [48], who study public (L), encrypted (M) and secret (H) communication channels in a concurrent setting, and Myers [33], who distinguishes between sensitivity of data structure length and content. Rafnsson et al. [42] show that PSNI composes under product. All three of [16], [37], [42] use trace semantics as a basis for behavioral equivalence. We show PSNI composes under all of our combinators, with stream semantics as the basis for behavioral equivalence, and with environments which are part of the computation model.

As an alternative to our trace- and stream-semantics, we could have chosen to express semantics as a function mapping input behaviors to output behaviors. While such a semantics would not give us a complete rule for composition, as the composition would suffer from the Brock-Ackerman anomaly [15], that issue is resolved by Widom et al. [53].

Asynchronous testing faces the same difficulties with blocking behavior as we face when putting two LTS_{IO} in interaction. Whereas Verhaard et al. [52] solve the issues by equipping a tester and the implementation under test with an input queue, our assumption of input totality effectively

means our interactive systems have input queues baked in. The Input/Output Automata model [27] is very similar to our LTS_{IO} model. It has input totality as a fundamental assumption, and is designed to reason about system composition and fairness. We have not seen this model applied in research on information-flow security. The concurrency semantics induced by our combinators is similar to that employed by signal processing formalisms, like Kahn networks [26] and dataflow programming languages, e.g. Functional Reactive Programming languages [18], [36]. Indeed, our combinators are reminiscent of the signal function constructors in [36].

Finally, end-to-end security is easier to achieve if we, alongside combinators which compose secure components securely, have combinators which *repair* insecurities. Rafnsson and Sabelfeld [43] give such a combinator which puts a logarithmic bound on leaks through progress in a ID-secure program, and Askarov et al. give a combinator which puts a logarithmic bound on leaks through timing observations [5]. Secure Multi-Execution [17], [44] is a promising new technique which, through program transformation or dynamic monitoring, modifies (modestly) the semantics of any program to become that of a secure program. Devriese and Piessens [17] prove that the approach enforces timing sensitive noninterference, while Rafnsson and Sabelfeld [44] show that, by relaxing the guarantee to PSNI, the semantics of secure programs can be modified less.

VII. CONCLUSION

We have presented a framework for secure composition. Coming back to the research questions from Section I, we have achieved generality along several dimensions: (i) our underlying systems are general labeled transition systems; (ii) we distinguish between the security level of message presence and content; (iii) our model incorporates environments as part of the system; (iv) our composition is facilitated by a rich set of combinators; and (v) we study both progress-sensitive and progress-insensitive security definitions. While the latter is a popular policy for practical tools, our findings point to the importance of the former in the context of secure system composition. Our findings also provide new insights on the impact of fairness for the security of system composition.

Future work includes investigation of composition in the presence of insecure components. Generalizing the results in this work and our previous results on limiting leakage by programs that satisfy PINI [43], we plan to extend our combinator set with enhanced combinators that are able to “repair” insecure components and make them readily pluggable into a secure (composed) system.

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APPENDIX

Theorem III.10. $p \in \text{PSNI} \implies p \in \text{PSNI}_E$.

Proof: Assume $p \in \text{PSNI}$. Let p_1, p_2 and s_1 such that $p_1, p_2 \in \text{PSNI}$, $s_1 \in \mathbb{S}_F$, $p_1 \simeq_\ell p_2$ and $p_1 \models p \xrightarrow{s_1}$ be arbitrary. Since $p_1 \models p \xrightarrow{s_1}$, we have $p \xrightarrow{s_1}$. Since $s_1 \in \mathbb{S}_F$ and $p \in \text{PSNI}$, there exists a $s_{1P} \in \mathbb{S}(p) \cap \mathbb{A}_\ell^\omega$ for which $s_1 \simeq_\ell s_{1P}$ and $\text{preserve}_{p, s_1}(\epsilon, s_{1P})$. Since $p_1 \models p \xrightarrow{s_1}$, we have $p_1 \xrightarrow{s_1^{-1}}$. Assume $s_1^{-1} \in \mathbb{S}_F$ for now (at the end of this proof, we explain how to adapt it to the scenario $s_1^{-1} \notin \mathbb{S}_F$). Since $p_1 \simeq_\ell p_2$, there exists a $\hat{s}_2 \in \mathbb{S}_F$ for which $p_2 \xrightarrow{\hat{s}_2}$ and $s_1^{-1} \simeq_\ell \hat{s}_2$. Since $p_2 \in \text{PSNI}$, there exists a $s_{2E} \in \mathbb{S}(p_2) \cap \mathbb{A}_\ell^\omega$ for which $\hat{s}_2 \simeq_\ell s_{2E}$ and $\text{preserve}_{p_2, \hat{s}_2}(\epsilon, s_{2E})$. To summarize, $s_{1P} \simeq_\ell s_1 \simeq_\ell \hat{s}_2^{-1} \simeq_\ell s_{2E}^{-1}$. We must show that there exists a $s_2 \in \mathbb{S}_F$ for which $p_2 \models p \xrightarrow{s_2}$ and $s_1 \simeq_\ell s_2$. We obtain s_2 using

$$\begin{aligned}
& \text{zip}(s_E, t, \bar{o}_P \cdot o_P \cdot s_P) \mid \bar{o}_P \simeq_\ell \epsilon \in \pi(o_P) \sqsubseteq \ell \\
&= \bar{o}_P \cdot o_P \cdot \text{zip}(s'_E, t, \bar{o}_P \cdot o_P \cdot s_P), \\
&\quad \text{where } \hat{s}_2 \simeq_\ell t \cdot (\bar{o}_P \cdot o_P)^{-1} \cdot s'_E \\
&\quad \wedge p_2 \xrightarrow{(t \cdot \bar{o}_P \cdot o_P)^{-1} \cdot s'_E} \\
&\quad \wedge \text{preserve}_{p_2, \hat{s}_2}((t \cdot \bar{o}_P \cdot o_P)^{-1}, s'_E) \\
& \text{zip}(\bar{o}_E \cdot o_E \cdot s_E, t, s_P) \mid \bar{o}_E \simeq_\ell \epsilon \in \pi(o_E) \sqsubseteq \ell \\
&= (\bar{o}_E \cdot o_E)^{-1} \cdot \text{zip}(s_E, t, (\bar{o}_E \cdot o_E)^{-1} \cdot s'_P), \\
&\quad \text{where } s_1 \simeq_\ell t \cdot (\bar{o}_E \cdot o_E)^{-1} \cdot s'_P \\
&\quad \wedge p \xrightarrow{t \cdot (\bar{o}_E \cdot o_E)^{-1} \cdot s'_P} \\
&\quad \wedge \text{preserve}_{p, s_1}(t \cdot (\bar{o}_E \cdot o_E)^{-1}, s'_P) \\
& \text{zip}(\bar{o}_E, t, \bar{o}_P) \mid \bar{o}_E^{-1} \simeq_\ell \bar{o}_P \simeq_\ell \epsilon \\
&= \bar{o}_E^{-1} \cdot o_P \cdot \text{zip}(\bar{o}'_E, t, \bar{o}_E^{-1} \cdot o_P \cdot \bar{o}'_P), \\
&\quad \text{where } \hat{s}_2 \simeq_\ell t^{-1} \cdot \bar{o}_E \cdot o_P^{-1} \cdot \bar{o}'_E \\
&\quad \wedge p_2 \xrightarrow{t^{-1} \cdot \bar{o}_P \cdot o_P^{-1} \cdot \bar{o}'_E} \\
&\quad \wedge \text{preserve}_{p_2, \hat{s}_2}(t^{-1} \cdot \bar{o}_P \cdot o_P^{-1}, \bar{o}'_E) \\
&\quad \wedge s_1 \simeq_\ell t \cdot \bar{o}_E^{-1} \cdot o_P \cdot \bar{o}'_P \\
&\quad \wedge p \xrightarrow{t \cdot \bar{o}_E^{-1} \cdot o_P \cdot \bar{o}'_P} \\
&\quad \wedge \text{preserve}_{p, s_1}(t \cdot \bar{o}_E^{-1} \cdot o_P, \bar{o}'_P).
\end{aligned}$$

By setting $s_2 = \text{zip}(s_{2E}, \epsilon, s_{1P})$, we get $p_2 \models p \xrightarrow{s_2}$ and $s_1 \simeq_\ell s_2$. This can be seen by observing that the middle parameter t during each corecursive call grows to include one more observable (until it contains all observables, after which it grows with unobservables), and that $t \preceq_\ell s_1$ and $t^{-1} \preceq_\ell \hat{s}_2$.

If $s_1^{-1} \notin \mathbb{S}_F$, then for the shortest prefix $t_1 \leq s_1^{-1}$ containing all output in s_1^{-1} , there is a \bar{o}_1 for which $p_1 \xrightarrow{t_1 \cdot \bar{o}_1}$. We then make sure to not zip beyond the last observable output in t_1 and still get the desired s_2 by replacing the last case in the definition of zip with one which evaluates to \bar{o}_P . ■

A. Combinators

1) Relaxed Cascade Feedback:

$$\frac{\frac{p_L \xrightarrow{o} p'_L \quad p_R \xrightarrow{o^{-1}} p'_R}{p_L \otimes p_R \xrightarrow{o} p'_L \otimes p'_R} \otimes_L^! \quad \frac{p_L \xrightarrow{o^{-1}} p'_L \quad p_R \xrightarrow{o} p'_R}{p_L \otimes p_R \xrightarrow{o} p'_L \otimes p'_R} \otimes_R^!}{p_L \otimes p_R \xrightarrow{i} p'_L \otimes p'_R} \otimes^?$$

2) Relaxed Cascade:

$$\frac{\frac{p_R \xrightarrow{o} p'_R}{p_L \otimes p_R \xrightarrow{o} p_L \otimes p'_R} \otimes^! \quad \frac{p_L \xrightarrow{i} p'_L}{p_L \otimes p_R \xrightarrow{i} p'_L \otimes p_R} \otimes^?}{p_L \otimes p_R \xrightarrow{o} p'_L \otimes p'_R} \otimes$$

3) Cascade:

$$\frac{\frac{p_R \xrightarrow{o} p'_R}{p_L \otimes p_R \xrightarrow{o} p_L \otimes p'_R} \otimes^! \quad \frac{p_L \xrightarrow{i} p'_L}{p_L \otimes p_R \xrightarrow{i} p'_L \otimes p_R} \otimes^?}{p_L \otimes p_R \xrightarrow{i} p'_L \otimes p'_R} \otimes$$

4) Product:

$$\frac{\frac{p_L \xrightarrow{o} p'_L}{p_L \oplus p_R \xrightarrow{o} p'_L \oplus p_R} \oplus_L^! \quad \frac{p_R \xrightarrow{o} p'_R}{p_L \oplus p_R \xrightarrow{o} p_L \oplus p'_R} \oplus_R^!}{p_L \oplus p_R \xrightarrow{i} p'_L \oplus p'_R} \oplus^?$$

5) Feedback:

$$\frac{\frac{p_L \xrightarrow{o} p'_L \quad p_R \xrightarrow{o^{-1}} p'_R}{p_L \odot p_R \xrightarrow{o} p'_L \odot p'_R} \odot_L^! \quad \frac{p_L \xrightarrow{o^{-1}} p'_L \quad p_R \xrightarrow{o} p'_R}{p_L \odot p_R \xrightarrow{o} p'_L \odot p'_R} \odot_R^!}{p_L \odot p_R \xrightarrow{i} p'_L \odot p'_R} \odot^?$$

6) Buffered Loop: Let $\lceil p \rceil = \lceil p \rceil_\epsilon$.

$$\frac{\frac{p \xrightarrow{o} p'}{\lceil p \rceil_t \xrightarrow{o} \lceil p' \rceil_{t.o}} \lceil \rceil^! \quad \frac{p \xrightarrow{i} p'}{\lceil p \rceil_t \xrightarrow{i} \lceil p' \rceil_t} \lceil \rceil^?}{\frac{p \xrightarrow{o^{-1}} p'}{\lceil p \rceil_{o.t} \xrightarrow{o^{-1}} \lceil p' \rceil_t} \lceil \rceil \quad \frac{p \xrightarrow{?} p'}{\lceil p \rceil_\epsilon \xrightarrow{?} \lceil p' \rceil_\epsilon} \lceil \rceil^\epsilon}$$