appraisals based on the number and distribution of the observations and the residual error obtained in fitting them to the long arc.

The prediction errors for satellites 1965 89A and 1968 2A appear to be consistent with the error in the Baker-Nunn field-reduced observations. Because these satellites were tracked with a high priority, many Baker-Nunn observations were obtained. It appears reasonable to expect that high-priority tracking, increased accuracy of field-reduced Baker-Nunn data, and larger numbers of laser observations should reduce the prediction error to less than 1 arcminute in the future.

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# Absolute and Differential Temperature Monitors Developed for Apollo Space Experiments

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Abstract-Investigation of the key parameters of temperature sensors suggests either platinum wire resistance sensors or thermistors. Thermistors have higher sensitivity and generally adequate stability, but have been scorned due to their nonlinear characteristics. A simple monitor is developed to linearize these nonlinear characteristics. A FORTRAN IV computer program is shown which optimizes all circuit values. The general technique should be applicable to other instrumentation problems.

## Introduction

THE SCIENTIFIC instruments which comprise the Apollo Telescope Mount require temperature monitors as part of the "housekeeping" requirements. The reason for this may be understood by realizing that these instruments are basically very high resolution telescopes. As an example, the ultraviolet scanning spectroheliometer (ATM Experiment S 055 A) which is being built for the Harvard College Observatory, Cambridge, Mass., requires a pointing accuracy of  $\pm 5$ seconds of arc [1]. With an instrument that requires

pointing to these accuracy levels, care mut be taken to monitor the temperature of many points along the case. In the event of a failure of the thermal control system, such a network of temperature monitors can make possible correction of the data, as the bending and defocusing of the instrument can be calculated. The worth of such a monitoring scheme is contingent upon the availability of low cost, high reliability temperature moni-

A brief survey indicated that commercial temperature monitors were not available which would meet the requirements. The prime requirements for the two basic monitors are as follows.

## Absolute Monitor:

Temperature Range 60 to 90°F Voltage Output 0 to 5 volts Accuracy  $+1^{\circ}F$ 

Differential Monitor:

Temperature Range -5 to +5°F Common Mode Range 60 to 90°F Voltage Output 0 to 5 volts Accuracy  $+0.5^{\circ}F$ 

With the requirements thus defined, a brief summary of

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temperature sensor characteristics will be presented.

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TABLE I
TEMPERATURE SENSOR COMPARISON

Sensor	Sensitivity (per °C)	Stability (°C/yr)	Merit (yr/°C²)
Quartz crystal	0.0035 percent	±0.01	.35 percent
Platinum resistance Silicon bulk	0.39 percent	$\pm 0.03$	13 percent
resistance Pt-Pt <sub>90</sub> RH <sub>10</sub>	0.7 percent	± 20	0.035 percent
thermocouple	0.004 percent	±1.5	0.003 percent
Thermistor	4 percent	$\pm 0.05$	80 percent

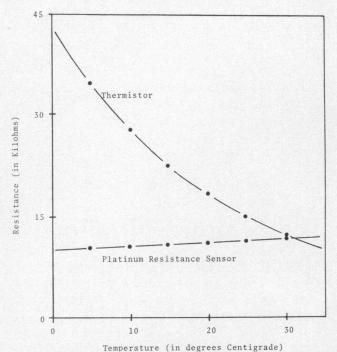


Fig. 1. Comparison of resistance-temperature characteristics of thermistors and platinum wire resistance sensors.

# SENSORS

Five basic types of temperature sensors were considered: linear temperature coefficient quartz crystals, platinum wire resistance sensors, silicon bulk resistance sensors (i.e., Sensistors¹), thermocouples, and thermistors. The two most important characteristics of these sensors are sensitivity and long term stability. Table I compares these characteristics for each of the named sensors. It should be emphasized that the stability figures are for "off the shelf" commercial units. In most cases these can be improved by about an order of magnitude, if selection or special processing is used.

In addition, Table I shows a figure of merit for each of the sensors. The figure of merit is calculated using

$$Merit = \frac{Sensitivity}{Stability} \cdot \tag{1}$$

As may be seen from Table I, the two sensors with the highest figure of merit are the platinum wire resistance sensor and the thermistor.

<sup>1</sup> Sensistor is a registered trademark of Texas Instruments, Inc.

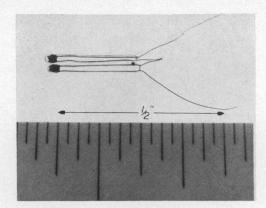


Fig. 2. Photograph of Fenwal type GB42SMM1 subminiature thermistor.

Fig. 1 compares the resistance–temperature characteristics for the platinum wire resistance sensor (normalized to  $10 \text{ k}\Omega$  at  $0^{\circ}\text{C}$ ) with the characteristics of the thermistor. The thermistor is much more sensitive, but also quite nonlinear (i.e.,  $R_{\text{th}} \approx \alpha \epsilon^{-\beta t}$ ) in comparison to the platinum resistance sensor. The high sensitivity means that the amplifier used to realize the monitor operates with low closed loop gain and high stability.

Thus the decision was made to use thermistors as temperature sensors for the Apollo monitors, contingent upon the development of an electronic system which will linearize the nonlinear characteristics of the thermistor. The thermistor selected was Fenwal Electronics, Inc. type GB42SMM1 Oceanographic Iso-Curve<sup>2</sup> thermistor. A photograph of this device is shown on Fig. 2. The scale at the bottom of the photograph has 1/32 inch graduations; thus the thermistor is about 9/32 inch long exclusive of leads. As Fig. 2 indicates the active elements of the thermistor are two beads of sintered metallic oxides about 1/32 inch in diameter, sealed in small glass tubes. The sensor has very low thermal mass and a dissipation constant of 1.6 mW/°C in fast moving air [2].

## ABSOLUTE TEMPERATURE MONITOR ELECTRONICS

A typical temperature monitor using a thermistor as the sensor is shown on Fig. 3. The output of the monitor is given by the equation

$$E_{\text{out}} = \frac{-V \cdot R_f}{R_{\text{in}} + R_{\text{eq}}} \cdot \frac{R_{\text{eq}}}{R_1} \tag{2}$$

where

$$R_{\rm eq} = \frac{R_1 \cdot R_{\rm th}}{R_1 + R_{\rm th}} \tag{3}$$

Conventional systems generally require that  $R_1\gg R_{\rm th}$  and  $R_{\rm in}\gg R_{\rm th}$ . Thus (2) reduces to

$$E_{\text{out}} = \frac{-V \cdot R_f \cdot R_{\text{th}}}{R_{\text{in}} \cdot R_1}$$
 (4)

The relationship between  $E_{\text{out}}$  (from (4)) and temperature is essentially similar in shape to the thermistor's

<sup>2</sup> Iso-Curve is a registered trademark of Fenwal Electronics, Inc.

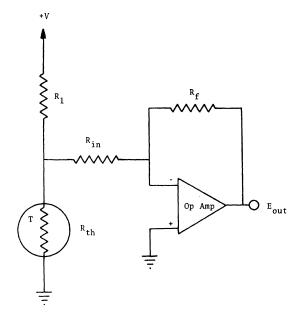


Fig. 3. Basic schematic diagram of absolute temperature monitor.

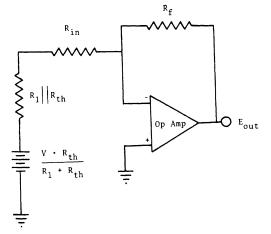


Fig. 4. Schematic diagram of absolute temperature monitor showing the effect of source resistance on gain.

resistance-temperature curve. The conventional system thus has a high degree of nonlinearity.

Reducing  $R_{\rm in}$  so that it is of the same order of magnitude as  $R_{\rm eq}$  causes the first term of (2) to vary in such a manner as to compensate for the varying thermal sensitivity of the thermistor. Fig. 4 illustrates this concept. The gain of the closed loop operational amplifier is given by

$$A = \frac{-R_f}{R_{\rm in} + R_{\rm eq}} \tag{5}$$

where  $R_{eq}$  is the Thevenin equivalent source resistance of  $R_{th}$  and  $R_1$ . Thus when  $R_{th}$  is highest (and the sensitivity is greatest), the amplifier gain is lowest, and vice versa. Thus selection of the optimum value for  $R_{in}$  causes the gain of the amplifier to compensate for the nonlinear thermistor, yielding a linear output. This monitor retains the high sensitivity of the thermistor while generating a nearly linear output voltage.

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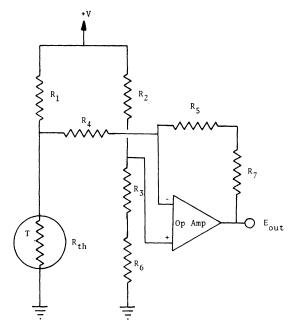


Fig. 5. Complete absolute temperature monitor schematic diagram showing all components.

Selection of the optimum value (for best linearity of the output voltage-temperature characteristics) of resistor  $R_{\rm in}$  can be a difficult problem. The selection may be done by using an empirical trial and error procedure with a breadboard of the monitor, or by writing a computer program to select resistor values that will yield optimum results the first time a monitor is built. An advantage of the latter approach is that future temperature monitors can be "designed" by punching a data card and running the already developed computer program.

A computer program was written to select all of the resistor values for the monitor shown in Fig. 5. The program is written in ASA basic FORTRAN IV to provide maximum "portability" from one computer to another. A listing of the program is included in the Appendix. The values of resistors  $R_1$  through  $R_5$  (in Fig. 5) are selected to coincide with standard 1 percent resistor values, while bracketing values are computed for  $R_6$  and  $R_7$  which are selected during calibration at final assembly time to set the offset and gain of the individual monitor. The two adjustable resistors permit a  $\pm 4$  percent variation to make up for other component tolerances. An example of a typical computer printout is shown in Fig. 6. The particular printout is for the 60 to 90°F absolute temperature monitor for the Apollo Telescope Mount experiments. The nonlinearity is less than  $\pm 0.050^{\circ}$ F throughout the range, and is greatest at the extremes; for most readings the error due to nonlinearity is less than  $\pm 0.025^{\circ}$ F, which is less than the error due to telemetry quantization. The linearity improvement achieved with this monitor configuration is shown as a function of monitor range in Fig. 7. One reason for the reduced degree of improvement is that at higher temperatures the thermistor deviates some-

#### ABSOLUTE TEMPERATURE MONITOR

BRIDGE VOLTAGE 10.0 VOLTS
TEMPERATURE RANGE 60.0 TO 90.0 DEGREES F
OUTPUT RANGE 0.0 TO 5.0 VOLTS
AMPLIFIER BIAS CURRENT 200. NA.

R1 = 0.954E R2 = 0.147E R3 = 0.261E R4 = 0.133E R5 = 0.154E R6 = 0.119E R7 = 0.695E	05 OHMS 06 OHMS 05 OHMS 05 OHMS 06 OHMS 04 OHMS NOM 04 OHMS NOM	RANGE 0.274		43E 04 OHMS
TEMP. (F)	DESIRED	ACTUAL	VOLTAGE	TEMP.
		VOLTAGE	DEVI	ATION
60.00000	0.00000	0.00793	0.00793	0.04760
61.00000	0.16666	0.17132	0.00465	0.02794
62.00000	0.33333	0.33528	0.00195	0.01170
63.00000	0.50000	0.49977	-0.00022	-0.00137
64.00000	0.66666	0.66474	-0.00192	-0.01152
65.00000	0.83333	0.83016	-0.00316	-0.01898
66.00000	1.00000	0.99599	-0.00400	-0.02401
67.00000	1.16666	1.16219	-0.00447	-0.02684
68.00000	1.33333	1.32871	-0.00462	-0.02773
69.00000	1.50000	1.49551	-0.00448	-0.02693
70.00000	1.66666	1.66255	-0.00411	-0.02467
71.00000	1.83333	1.82979	-0.00353	-0.02122
72.00000	2.00000	1.99719	-0.00280	-0.01682
73.00000	2.16666	2.16471	-0.00195	-0.01172
74.00000	2.33333	2.33230	-0.00102	-0.00616
75.00000	2.50000	2.49993	-0.00006	-0.00039
76.00000	2.66666	2.66755	0.00088	0.00533
77.00000	2.83333	2.83513	0.00179	0.01078
78.00000	3.00000	3.00261	0.00261	0.01570
79.00000	3.16666	3.16997	0.00331	0.01987
80.00000	3.33333	3.33717	0.00384	0.02305
81.00000	3.50000	3.50416	0.00416	0.02499
82.00000	3.66666	3.67091	0.00424	0.02548
83.00000	3.83333	3.83738	0.00404	0.02428
84.00000	4.00000	4.00352	0.00352	0.02116
85.00000	4.16666	4.16932	0.00265	0.01592
86.00000	4.33333	4.33472	0.00139	0.00834
87.00000	4.50000	4.49970	-0.00029	-0.00179
88.00000	4.66666	4.66421	-0.00244	-0.01469
89.00000	4.83333	4.82824	-0.00509	-0.03055
90.00000	5.00000	4.99173	-0.00826	-0.04956

Fig. 6. Results of the computer program which has optimized resistor values for the range of 60 to 90°F.

what from exponential characteristics which are so amenable to linearization by this method. Even so, the nonlinearity is less than 1 percent of the range for most of the cases shown on the graph.

#### DIFFERENTIAL TEMPERATURE MONITOR ELECTRONICS

The second type of monitor required for the Apollo experiments is a differential temperature monitor. The output voltage from such a monitor should be a linear function of the difference between two absolute temperatures. Fig. 8 shows one method to measure temperature differences. This method uses an operational amplifier to take the difference between two absolute monitor outputs. The main drawback with this scheme is that it uses a fairly large number of parts. The three operational amplifiers use more power (about 90 to 120 mW) than is desirable, so a circuit was developed to take the difference at the input to a single amplifier. This circuit is shown on Fig. 9. It should be noted that  $R_1$ ,  $R_2$ , and  $R_{th1}$  make up a network similar to  $R_1$ ,  $R_{in}$ , and  $R_{\rm th}$  in Fig. 3;  $R_3$ ,  $R_4$ , and  $R_{\rm th2}$  make up a similar network. The currents  $i_1$  and  $i_2$  are the same nearly linear function of temperature as  $E_{\text{out}}$  for the absolute temperature monitor. Thus in Fig. 9,

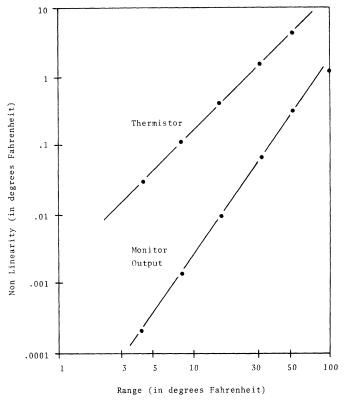


Fig. 7. Graph comparing linearity of the temperature monitor output with the thermistor linearity.

$$i_i = \alpha_1 + \alpha_2 \cdot T_i \quad \text{for} \quad i = 1 \text{ or } 2. \tag{6}$$

Since  $i_3 = i_1 - i_2$ , the terms containing  $\alpha_1$  cancel leaving

$$i_3 = \alpha_2 \cdot (T_1 - T_2). \tag{7}$$

Analysis of the operational amplifier indicates that

$$E_{\text{out}} = R_5 \cdot \alpha_2 \cdot (T_1 - T_2) \tag{8}$$

which is the exact relationship desired for the differential temperature monitor. The circuit for the complete differential temperature monitor showing gain equalization, output offset, and differential gain adjusting resistors  $R_9$ ,  $R_{10}$ , and  $R_{11}$ , respectively, is shown in Fig. 10. Note that this circuit uses less than a dozen precision resistors and a single operational amplifier to take high accuracy measurements of temperature differentials.

As was the case with the absolute temperature monitor, a computer program is used to determine the optimum values of the resistors which comprise the circuit. Resistors  $R_1$  through  $R_8$  are selected to be standard 1 percent values while the adjustment resistors  $R_9$  through  $R_{11}$  are bracketed to less than a decade to ease the procurement task.

### MONITOR PERFORMANCE

Accepting the thermistor manufacturer's specifications for long term stability of  $\pm 0.05^{\circ}$ C per year, the combined inaccuracy of the absolute temperature monitors is about  $\pm 0.13^{\circ}$ F [2]. This figure includes the worst case nonlinearity and sensor instability. Amplifier variations

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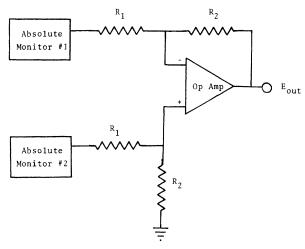


Fig. 8. Differential temperature monitor built with two absolute temperature monitors.

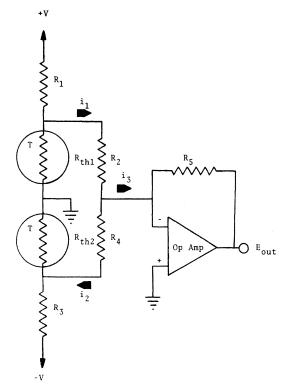


Fig. 9. Schematic of the basic differential temperature monitor which uses a single operational amplifier.

tions and component instability have a negligible effect on the output (as a result of low closed loop gain used). Laboratory tests indicate that the thermistor stability is generally better than about  $\pm 0.02^{\circ} F$  per year. Using this figure results in an overall accuracy for the monitor of 0.07°F. The accuracy of the differential temperature monitors is about  $\pm 0.1^{\circ} F$  if the thermistors are assumed to age in the same manner.

As with most space equipment, power consumption is a very important parameter. With these monitors the operational amplifier uses most of the power. For example, using a Fairchild Semiconductor type  $\mu$ A709A operational amplifier with  $\pm 10$  volt supplies, the

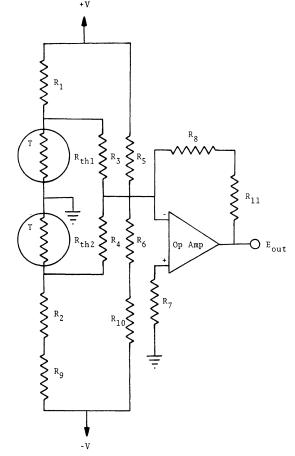


Fig. 10. Complete differential temperature monitor schematic diagram showing all components.

monitors each require about 30 to 40 mW [3]. If the operational amplifier was replaced with a National Semiconductor Corporation type NH0001 hybrid operational amplifier, the power consumption should drop to about 4 mW for the absolute monitor and about 5 mW for the differential monitor [4].

#### SUMMARY

Temperature monitors have been developed using thermistors as the temperature sensors. Thermistors are a nearly ideal choice for the application, since they have high sensitivity and high stability, the only drawback is the need to linearize the exponential resistance-temperature characteristics. Circuits have been developed which monitor absolute as well as differential temperatures. These monitors are simple in concept. Indeed, to reduce the effort required to change the range, a computer program is used to optimize all resistor values.

The linearization technique should be applicable to many other resistive sensor instrumentation problems.

# APPENDIX

ral amplifier uses most of the power. For exusing a Fairchild Semiconductor type  $\mu$ A709A listed below. The three subroutines RSTD, RTH, and ral amplifier with  $\pm 10$  volt supplies, the SQFIT are listed after the listing of the main program. Authorized licensed use limited to: IEEE Xplore. Downloaded on September 27,2024 at 01:58:00 UTC from IEEE Xplore. Restrictions apply.

# THE FORTRAN IV PROGRAM AND SUBROUTINES

```
THE FORTRAN IV PRO

COMMON AT(51): R1. V

READ (2:2) V*P-TMIN-TMAXEMINEMAX*BIAS*I:10EG.DSPAN

FEMPR 2

READ (2:2) V*P-TMIN-TMAXEMINEMAX*BIAS*I:10EG.DSPAN

FEMPR 2

READ (2:2) V*P-TMIN-TMAXEMINEMAX*BIAS*I:10EG.DSPAN

FEMPR 3

FEM
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                      ENON=EREF+(R5+R7)*(BIASI+(EREF*(TH(J)+R1)-TH(J)*V)/(R1*(TH(J)+R4)+TEMPR128

ITH(J)*R4))

DELTA=MON+CALC

TEMPR129

DELTA=MON+CALCA

TEMPR129

WRITE (15:36) T-ECALC-EMON-DELTA-ERTEM

TEMPR139

IF (DSPAN) 37:1-38

DSPAN-DSPAN

DSPAN-DSPAN

DSPAN-DSPAN

MRITE (15:36) T-ECALC-EMON-DELTA-ERTEM

TEMPR139

IF (DSPAN) 37:1-38

DSPAN-DSPAN

DSPAN-DSPAN

MRITE (15:36) T-ECALC-EMON-DELTA-ERTEM

TEMPR139

IF (DSPAN) 37:1-38

DSPAN-DSPAN

DSPAN-DSPAN

MRITE (15:36) T-ECALC-EMON-DELTA-ERTEM

TEMPR139

IF (DSPAN) 37:1-38

TEMPR139

IF (PSPAN)

MRITE (15:36) T-ECALC-EMON-DELTA-ERTEM

TEMPR139

IF (STAND-MAN-DSPAN)

MRITE (15:36) T-ECALC-EMON-DELTA-ERTEM

TEMPR139

IF (STAND-MAN-DSPAN)

MRITE (15:36) T-ECALC-EMON-DELTA-ERTEM

TEMPR139

IF (STAND-MAN-DSPAN)

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IF (STAND-MAN-DSPAN)

TEMPR139

IF (STAND-MAN-DSPAN)

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IF (STAND-MAN-DSPAN)

TEMPR139

TEMPR139

TEMPR149

                                                                          DIMENSION TH(42)* TCOM(6)* EO(6)

COMMON RT(51)* R1* V

READ (2:2) V:P*IMIN*IMAX*EMIN*EMAX*BIAS*I*IDEG*DSPAN
FORMAT (770.6**I1*A1*F8.0)
IF (V) 4:3*4
STOP 7734
BIAS1*81A5**000000001
STEP=(TMAX*TMIN)**.02
DO 5 J=19:51
T=TMIN*STEP*FLOAT(J=1)
RT(J)=RTH(T*1)
R=V4RT(1)
R=V4RT(1)*RT(1)*RT*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR*IDEAR
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             EMON=EREF+(R5+R7)*(BIASI+(EREF*(TH(J)+R1)=TH(J)*V)/(R1*(TH(J)+R4)+TEMPR128
1TH(J)*R4))
   11
   12
13
   14
   15
   22
23
   24
25
   26
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   FUNCTION RSTD (X)
T=X
I=0
I=0
IF (X) 9,9+1
IF (T=100.+) 2.3+3
T=10.0+7
I=1-1
GO TO 1
IF (T=1000.+) 5.4.4
T=:10+7
I=:10+7
   27
28
   29
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             IF (T-1000+, ,...

I=(1-)

GO TO 3

RSTDD-100-

IF (ABSIT-RSTD)-.01214*RSTD) 8.8.7

RSTDD-RSTDD-1.02428

GO TO 6

RSTDD-FLOAT(IFIX(RSTD+.5))*10.**I

RETURN

FND
   30
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             FUNCTION RTH (T+1)
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                             FUNCTION SQFIT (R)
DIMENSION B(50)
COMMON RT(51). R1. V
                                                         R7=R7-R5
R6=(R3-R6)**(1.*(Al1-Al)/100*)
R6=(R3-R6)**(1.*(Al1-Al)/100*)
R6=(R3-R6)**(1.*(Al1-Al)/100*)
R5=(R5)*(1.*(Al1-Al)/100*)
R5=(R5)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(Al1-Al)-R6)*(1.*(
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                S=0.
SS=0.
                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                   DO 35 J=1:N
T=TMIN+STEP+FLOAT(J=1)
ECALC=EMIN+(EMAX-EMIN)+STEP+FLOAT(J=1)/(TMAX-TMIN)
```

The criteria for stopping the various iterative loops are rather complex and will not be listed here [5].

Data input is by a single data card for each monitor. The data format is as follows.

Columns	Quantity	Units	Symbol
11-20 1 21-30 1 31-40 1 41-50 1 51-60 1 61-70 6 71 1	Bridge voltage Maximum thermistor power dissipation Minimum temperature Maximum temperature Minimum voltage output Maximum voltage output Operational amplifier bias current If input temperatures * are in °F = 1 If input temperatures * are in °F = "F" If input temperatures * are in °C = "C" Differential temperatures * are in °C = "C"	degrees* degrees* volts volts nA	V PD TMIN TMAX EMIN EMAX IBIAS

If DSPAN < 0 only a differential monitor will be "designed"; if DSPAN = 0 only an absolute monitor will be "designed"; and if DSPAN > 0 both types will be "designed." For the differential monitors TMIN and TMAX define the extremes of the common mode range, and DSPAN is the differential range to be covered.

## REFERENCES

- [1] "Design and performance specification, UV scanning polychromator spectroheliometer, ATM experiment S-055A," Ball Broth-
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  "Oceanographic iso-curve sub-mini-probe thermistors," Fenwal
- Electronics, Inc., Framingham, Mass., Data Sheet D-10, 1966. "µA709A high performance operational amplifier," Fairchild Semiconductor, Mountain View, Calif., 1967. "NH0001 operational amplifier," National Semiconductor Corp., Santa Clara Colif. 1968.
- Santa Clara, Calif., 1968.

  [5] E. E. Swartzlander, Jr., "Development of absolute and differential temperature monitors," M.S.E.E. thesis, University of Colorado, Boulder, 1969.