Material Needs and Measurement Challenges for Advanced Semiconductor Packaging: Understanding the Soft Side of Science

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Abstract—This Perspective builds upon insights from the National Institute of Standards and Technology (NIST)organized workshop, "Materials and Metrology Needs for Advanced Semiconductor Packaging Strategies," held at the 35th annual Electronics Packaging Symposium in Binghamton, NY, on September 5, 2024. It outlines critical challenges and opportunities related to polymer-based "soft" materials in advanced semiconductor packaging, with emphasis on polymer science, measurement science (metrology), and the strategic development of Research-Grade Test Materials (RGTMs). These efforts, led by the NIST CHIPS team, aim to advance the fundamental understanding of structure-property-processing relationships, promote standardized guidelines and innovative methods for material characterization, and accelerate the development, qualification, and adoption of next-generation packaging materials. The Perspective also distills key insights from the panel discussion with industry experts, emphasizing the need for close collaboration among materials scientists, process engineers, and metrology experts to enable a holistic strategy, further highlighting the importance of cross-sector partnerships among industry, academia, and government to address pressing challenges in packaging materials and processes.

Index Terms— Advanced semiconductor packaging, cure kinetics, heterogeneous integration (HI), metrology, moisture

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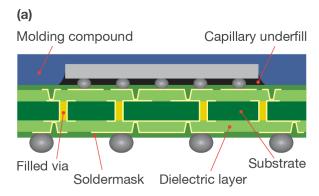
reliability, open access, open data, polymeric materials, residual stress, research-grade test materials (RGTMs), standardization and reproducibility, structure-property relationships, thermoset resins, underfill materials, viscoelastic modeling, warpage.

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I. INTRODUCTION

OLYMERIC materials—soft, versatile compounds composed of long, flexible, and repeating molecular chains—have been used in semiconductor packaging for decades. Since the early 1960s, polymeric packaging materials have primarily served a straightforward function: mechanically encapsulating chips and securely attaching them to circuit boards [1]. Historically, polymer-related challenges, such as coefficient of thermal expansion (CTE) mismatch and moisture absorption (the so-called "popcorning" effect [2]), were primarily associated with mechanical reliability concerns rather than directly affecting device performance. Many of the polymeric materials introduced decades ago, including epoxies, silicones, and polyimides, remain remarkably similar to those still widely used today. They continue to fulfill critical roles such as encapsulants, underfills, molding compounds, adhesives, thermal interface materials (TIMs), and substrate dielectric layers (Fig. 1), providing essential electrical insulation, mechanical support, and environmental protection for advanced packages [3, 4].

Today, the semiconductor industry is undergoing a transformative shift towards heterogeneous integration (HI) [5] to overcome the limitations of traditional transistor scaling and achieve enhanced performance, miniaturization, and cost efficiency. Over the next decade, emerging technologies—including 5G/6G wireless networks, artificial intelligence (AI), and high-performance computing (HPC), will rely heavily on advanced packaging enabled by HI [6]. As packaging architectures evolve into increasingly intricate 3D structures with finer feature sizes, smaller gap heights, flexible chiplets, and diverse materials interfaces, the demands on polymeric materials have become significantly more stringent. A broad spectrum of properties—including mechanical, thermal, electrical, and chemical-must now be simultaneously optimized. For instance, the transition from rigid chips to flexible electronic systems demands polymers with enhanced flexibility, toughness, and long-term fatigue resistance [7]. Likewise, 5G/6G applications require precise



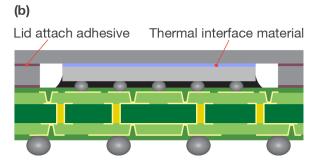


Fig. 1. Polymer-based materials used in advanced packaging. (a) An overmolded flip chip package. (b) A lidded flip chip package.

control over dielectric properties to minimize transmission loss and maintain signal integrity in high-frequency operating environments [8, 9]. Furthermore, integrating a wide range of materials into complex 3D multi-chip structures presents significant challenges in materials selection, process optimization, and interconnect reliability [10].

These evolving requirements underscore the need to reexamine legacy packaging resins and develop high-performance polymer-based materials tailored for next-generation technologies [11]. Modern formulations often involve multiple resins, multifunctional additives, and inorganic fillers, significantly increasing the complexity of material design and manufacturing. This is further compounded by the industry's heavy reliance on overseas material suppliers [12], limited transparency into material composition and performance data, and inadequate characterization standards and metrology methods, all of which create additional hurdles in materials development and implementation. At the same time, longstanding challenges—such as thermomechanical reliability concerns intrinsic to polymer behavior—persist and are further amplified in the context of HI.

Addressing these multifaceted challenges requires a robust and integrated materials science framework underpinned by advanced and precise metrology techniques [13]. This *Perspective* explores key challenges and opportunities related to polymer-based materials in advanced semiconductor packaging, with a focus on polymer science fundamentals, measurement science (metrology), and the strategic implementation of Research-Grade Test Materials (RGTMs).

These components are essential for deepening the fundamental understanding of structure-property-processing relationships in complex polymer formulations, promoting standardized guidelines and innovative methods for material characterization, and accelerating the development, qualification, and adoption of next-generation packaging materials.

II. POLYMER-BASED MATERIALS IN ADVANCED PACKAGING

Given the diverse roles and growing functional demands of polymer-based materials in advanced packaging applications, careful optimization of their chemical composition, mechanical properties, thermal stability, and dielectric performance is essential. In this section, we briefly revisit key principles in polymer science—spanning both polymer physics and chemistry—that lay the groundwork for understanding structure-property-processing relationships in these materials.

The glass transition temperature (T_g) is often reported as a single value in material datasheets—but the underlying physics is far more complex and intriguing [14]. As Nobel Laureate P. W. Anderson famously remarked, the glass transition is "perhaps the deepest and most interesting unsolved problem in solid-state physics [15]." T_g marks the temperature range over which a crosslinked polymer gradually transitions from a rigid, glassy state to a soft, rubbery state, dictating the material's physical and mechanical properties, processing conditions, and application temperature limits [16].

However, the glass transition as measured in the laboratory is not a thermodynamically defined phase change; rather, it is a kinetic phenomenon that arises from the dramatic slowdown of molecular motions as the material cools and falls out of equilibrium [14, 16, 17]. As such, $T_{\rm g}$ depends on the intrinsic polymer structure as well as external factors such as *timescale* (rate or frequency), *thermal history* (how the glass is formed), and *measurement method* (i.e., the property being probed). The value of $T_{\rm g}$ increases by approximately (3 to 5) °C for every order-of-magnitude increase in cooling rate [17, 18]. Furthermore, $T_{\rm g}$ can shift under real-world operating environments—through mechanisms such as physical aging [19], stress relaxation, and moisture ingress, which alter the polymer relaxation dynamics and effectively modify its glass transition behavior.

The glass transition has many manifestations, including changes in heat capacity, specific volume, modulus, viscosity, and dielectric constant. Each manifestation is measured by a different technique. Thus, in practice, $T_{\rm g}$ can vary significantly depending on how it is measured—differential scanning calorimetry (DSC), dynamic mechanical analysis (DMA), rheometry, thermomechanical analysis (TMA), dilatometry, or dielectric analysis (DEA) each detect different property changes [20]. For example, $T_{\rm g}$ values from DMA loss tangent peaks can be (15 to 20) °C higher than those measured by dilatometry on cooling at 1 °C/min—a discrepancy that can lead to substantial error when predicting rate-dependent processes [21]. Even within a single method

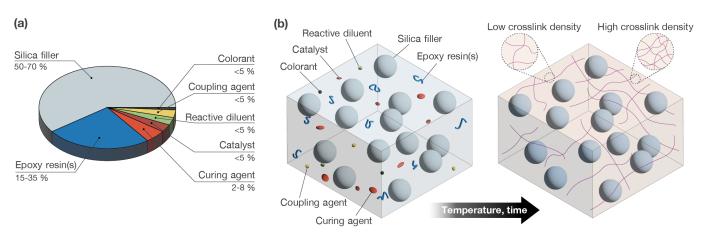


Fig. 2. (a) Typical composition of an epoxy-based underfill material shown as a pie chart; (b) Molecular-level schematic of the formulation highlighting the dispersion of functional additives and fillers within the epoxy matrix. Upon thermal curing, the epoxy resins undergo crosslinking reactions to form a three-dimensional polymer network.

like DSC, $T_{\rm g}$ may be reported using different criteria for the midpoint of the heat capacity step change, such as the inflection point, half-height, or fictive temperature. This is simply because the transition occurs over a temperature range rather than as a sharp, single point. Therefore, reporting $T_{\rm g}$ without specifying the measurement technique and analysis method is inadequate for meaningful interlaboratory comparisons.

Advanced formulations for polymer-based packaging materials are highly complex, often incorporating significant amounts of inorganic fillers along with a variety of additives to meet performance requirements (**Fig. 2a**). In thermoset polymers, which constitute the majority of polymer-based packaging materials, the curing process typically occurs through thermal crosslinking—a chemical reaction at elevated temperatures in which resin molecules polymerize into longer chains and eventually link together to form a rigid, three-dimensional network (**Fig. 2b**). As curing progresses, T_g increases from the initial value of the uncured monomer mixture (T_{g0}) to the fully cured value ($T_{g\infty}$).

The ability to control crosslink density and polymer network structure through tailored polymer chemistry is central to designing base resins that define the basic physical and mechanical properties of the material. In epoxy-based formulations—a nearly century-old chemistry—the choice of monomers, curing agents, and catalysts, along with their stoichiometry and reactivity, directly influences reaction kinetics and the architecture of the resulting polymer network [22]. Factors such as mono- vs. multi-functional epoxides, aliphatic vs. aromatic curing agents, type of curing agent (e.g. anhydride, amine, or Lewis acid), or the use of reactive diluents and modifiers can significantly impact the rate of polymerization (cure kinetics), gelation time, and the onset of vitrification, ultimately affecting storage life, the processing window, and final material properties [23]. Additionally, insufficient mixing of reactants prior to the gel point could result in network and morphological inhomogeneities [22], such as regions of low versus high

crosslink density, as shown in Fig. 2b.

Fillers add yet another layer of complexity to the resin systems. Optimizing filler content, surface chemistry, and particle size and distribution is crucial to balance performance with manufacturability [24]. For example, in capillary underfill formulations, low viscosity is required for efficient dispensing (Fig. 3a), while high filler loading is necessary to reduce CTE. However, increasing filler content typically raises viscosity and complicates flow behavior [25], introducing effects such as thixotropy—a reversible, timedependent decrease in viscosity under shear—and yield stress, both of which can adversely impact processability [26, 27]. This interplay between filler-resin interactions and the evolving polymer network during curing highlights the need for accurate chemoviscosity models, which describe how viscosity changes with the degree of cure, time, and temperature [28]. In practice, underfills are frequently dispensed at elevated temperatures to reduce viscosity, accelerate the process, and minimize the presence of voids in Similarly, for advanced molding the cured underfill. compounds used in compression molding of fan-out wafer/panel-level packaging (FO-WLP/FO-PLP), accurate prediction material behavior during curing (chemorheology) is essential for effective warpage management [29, 30]. Process-induced warpage—arising from CTE mismatch between different components, cure shrinkage, and hygrothermal stressors (Fig. 3b)—can lead to significant reliability risks, such as die shift, interconnect misalignment, and interfacial delamination [31]. mechanical behavior, adhesion, and stress-related performance of polymeric composites are critically important in this context, as they directly affect warpage control, stress reduction, and interfacial reliability in the final package. Addressing these challenges requires an integrated approach that combines advanced materials design, predictive modeling, and process control [32, 33].

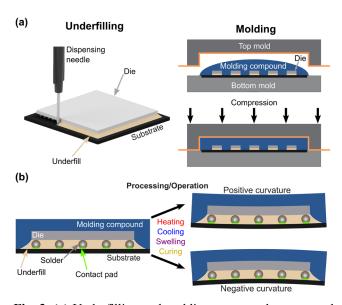


Fig. 3. (a) Underfilling and molding steps used to encapsulate and protect chips. **(b)** Schematic cross-sections depicting how processing conditions can induce warpage.

III. INTRINSIC CHALLENGES BY POLYMERIC MATERIALS IN ADVANCED PACKAGES

Despite their versatility and cost efficiency, polymeric materials inherently present challenges in semiconductor packaging due to their *cure-dependent*, *temperature-sensitive*, *hygroscopic*, and *viscoelastic* (time-dependent) behaviors. In this section, we highlight several important practices and insights from polymer research that help inform material processing strategies.

A. Accurate Cure Kinetics for Advanced Resin Formulations

Understanding cure kinetics and designing optimized curing schedules are essential for achieving desired material performance and improving manufacturing productivity. Advanced resin formulations with highly loaded fillers often exhibit complex, multi-step reactions that pose challenges for both measurement [34] and kinetics modeling [35-37]. Recent advances in kinetic analysis software have enabled the extraction of kinetic parameters from thermal analysis data that account for both chemical and diffusion-controlled Accurate cure kinetics models enable kinetics [38]. predictions and simulations under varying processing conditions (time and temperature), improve on-tool efficiency (e.g., post-mold cure design), and help reduce energy costs. These models are also foundational for enabling predictive modeling of process-dependent properties chemoviscosity and chemorheology [39, 40]. As noted earlier, T_g is a sensitive indicator of the degree of cure [41] especially during the final stages where, for example, a 1% increase in conversion between 90 % and 100 % was observed to raise T_g by about 5 °C in an epoxy system [42, 43]. Therefore, establishing the T_g vs. conversion relationship is imperative when optimizing the cure profiles.

B. Process Pathways Influence Residual Stress Development

Residual stress remains a longstanding challenge in composites manufacturing. Effective process optimization requires careful stress management throughout every stage of production. The buildup of residual stress is highly dependent on the processing pathway [44, 45], specifically, the temperature and time profile the material experiences to reach its final degree of cure. Therefore, optimizing cure schedules requires a deep understanding of the material's transformation behavior during processing.

The Time-Temperature-Transformation (TTT) diagram provides a conceptual framework for understanding the key transitions during the isothermal curing of a thermoset [46, 47]. **Fig. 4** highlights two key critical transitions that influence manufacturability and residual stress development: gelation and vitrification.

Gelation (red curve) marks the onset of a space-spanning, infinite molecular network [48], characterized by an exponential increase in viscosity that theoretically diverges at the gel point and coincides with the initial emergence of an equilibrium elastic modulus [49, 50]. Beyond this point, the material loses its ability to flow, significantly impacting processability (e.g., dispensing and molding). Critically, after gelation, processinduced stress begins to accumulate as the material transitions from a viscous liquid to a gel-like, viscoelastic solid. Vitrification (S-shaped blue curve) occurs when the $T_{\rm g}$ of the material exceeds the cure temperature (T_c) [46]. As discussed earlier, $T_{\rm g}$ rises from the initial uncured $T_{\rm g0}$ towards the ultimate $T_{g\infty}$ as crosslinking proceeds. Once vitrified, polymer chain mobility becomes highly restricted, dramatically slowing the reaction rate and kinetically trapping the material in a "diffusioncontrolled" glassy state [51]. If vitrification occurs prematurely—before sufficient crosslinking—this can result in high residual stress, poor adhesion, and long-term reliability concerns. With a thorough understanding of the reaction kinetics of a given material system, one can adjust the time and temperature of process steps to maintain the cure temperature

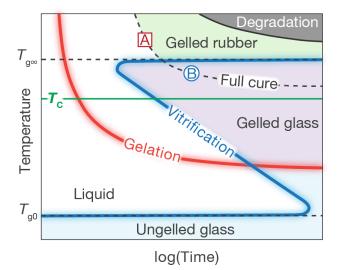


Fig. 4. Schematic of a typical Time-Temperature-Transformation (TTT) diagram illustrating the key phase transitions during thermoset curing.

above the $T_{\rm g}$ of the partially cured material to minimize those issues.

Direct visualization of the TTT diagram offers valuable guidance for process design. At a selected isothermal $T_{\rm c}$ (green line, **Fig. 4**), the material first undergoes gelation and then vitrification as curing progresses, ultimately forming a "gelled glass" state. To produce a partially cured, shape-retaining "B-stage" material, a lower $T_{\rm c}$ should be chosen such that it intersects only the vitrification curve. In contrast, achieving full cure requires selecting a $T_{\rm c}$ above $T_{\rm go}$ that avoids vitrification and intersects only the gelation curve—while remaining below the degradation threshold. In this case, the material undergoes rapid gelation and proceeds into the "gelled rubber" region.

To illustrate the impact of cure pathways on residual stress development, Fig. 4 compares two material states, A and B, following different cure conditions: profile $A(t_A, T_A)$ and profile $B(t_{\rm B}, T_{\rm B})$. Although both paths reach the same degree of curefalling along the same isoconversion contour in the TTT diagram—they result in different residual stresses. Profile A achieves full cure through a shorter ($t_A \le t_B$), higher-temperature $(T_{\rm A} > T_{\rm B})$ schedule that avoids vitrification $(T_{\rm A} > T_{\rm g\infty})$, resulting in lower residual stress. In contrast, profile B follows a longer, lower-temperature cure $T_{\rm B} < T_{\rm g\infty}$, passing through vitrification and leading to greater stress accumulation. This comparison highlights the critical role of gelation and vitrification pathways on stress development during thermoset curing. By leveraging TTT-informed chemorheological modeling, engineers can finetune processing conditions to minimize residual stress, maximize performance, and improve package reliability.

C. Hygrothermal Cycling During Operation

Residual stress contributes to reliability concerns in packages. Environmental and operational stressors—such as temperature fluctuations and moisture ingress (hygroscopic swelling)—can accelerate material degradation and exacerbate interface failures, ultimately reducing the device's service life.

For example, underfill materials subjected to repeated thermal cycling are prone to developing microcracks and delaminating from multiple interfaces, potentially resulting in device failure [31, 52]. Those effects are most frequently due to mismatches in the CTE among the package, die, underfill, and substrate. In such cases, thermal cycling can fatigue solder joints, leading to electrical failure. Accurate measurements of CTE, modulus, and $T_{\rm g}$ are essential for developing mechanical models that predict the cycle life of an electronic assembly. In general, it is undesirable for a material's T_g to fall within the operating temperature range of the hardware, due to the significant change in CTE that occurs near the $T_{\rm g}$. While the decrease in modulus associated with the glass transition may partially mitigate the consequences of CTE mismatch [53, 54], comprehensive and accurate property data are essential for predictive modeling and reliability assessment. In addition to mechanical failures, moisture further complicates performance by affecting functionality—through moisture diffusivity and solubility that degrade dielectric properties and compromise signal integrity, particularly at the high switching speeds of 5G and 6G technologies. I/O signals that are sharp and high-fidelity in a dry environment can become lossy or distorted as moisture infiltrates the package. This functional degradation remains a significant concern due to constant fluctuations in moisture levels over years of use.

Understanding the evolution of residual stress and functional material properties under relevant hygrothermal conditions is important [55, 56]. Equally critical is evaluating how interfacial material properties change over time in response to these environmental stressors. Maintaining robust adhesion between polymers and adjacent materials—such as metals, oxides, or other organic layers—remains a persistent challenge in advanced packaging [57, 58]. Overcoming these challenges will require not only improved material formulations, but also advanced characterization techniques capable of assessing these behaviors under application-relevant conditions.

D. Accurate Property Measurements and Modeling

Accurate modeling of advanced packages depends on highquality, reliable material property data. Polymers are viscoelastic, exhibiting time-, frequency-, and temperaturedependent mechanical properties [59]. Among these, the viscoelastic bulk modulus [60, 61] and Poisson's ratio [62-64] are important for predicting stress evolution [65]; however, their time and temperature dependencies are often overlooked. While simplified models that assume constant or purely elastic values may ease computation, they can lead to large discrepancies between simulation and experimental results, especially in multi-layered structures with large CTE mismatches. For example, incorporating a temperaturedependent Poisson's ratio into thermal warpage simulations improved prediction accuracy by a factor of four compared to constant-property models [66]. As packaging technologies continue to scale toward thinner geometries and finer-pitch interconnects, incorporating viscoelastic, time-dependent properties into simulations becomes not only beneficial but essential for reliable stress analysis and process design.

"Modeling without metrology is imagination."

—William (Bill) Chen, Advanced Semiconductor Engineering, Inc., at the 2024 Electronics Packaging Symposium

Without rigorous metrology and standardized measurement methods to facilitate interlaboratory comparisons (e.g., variations in $T_{\rm g}$ across different techniques as discussed earlier), predictive modeling remains limited. Accurate and reproducible data are critical for building reliable simulations and enabling digital twins in thermoset manufacturing. Robust metrology not only enhances modeling capabilities, but also enables fast materials screening and supports the successful deployment of new materials from research to production.

IV. ADVANCING METROLOGIES TO ADDRESS MEASUREMENT CHALLENGES

Given the complex, multi-material, and multi-scale interactions that govern the performance of polymeric

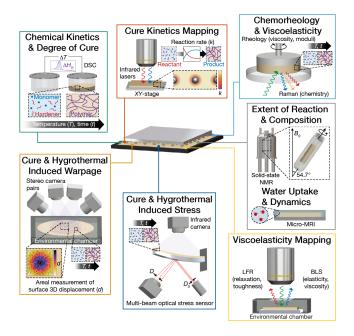


Fig. 5. Advanced metrology techniques for polymer-based packaging material characterization in the NIST CHIPS metrology project [67].

packaging materials, advanced characterization techniques are essential for gaining insights into structure—property—processing relationships and informing the development of predictive models. At the same time, ensuring measurement consistency and reproducibility across laboratories and tools is equally important. Such consistency is crucial for evaluating material properties, optimizing processes, and improving reliability assessments.

Fig. 5 presents a suite of advanced metrology techniques being developed and refined through our CHIPS Metrology Project, Accurate Cure Kinetics, Stress, Mechanical Properties and Warpage Measurements for Next-Generation Microelectronics Packaging under Device Conditions [67]. These tools have been integral in developing accurate cure kinetics and chemorheological models by implementing complementary approaches. These include measuring the heat of reaction directly via DSC, capturing viscoelastic transitions during polymerization simultaneous rheo-Raman spectroscopy [37], monitoring chemical conversion by fast Fourier transform infrared spectroscopy (FTIR) [68, 69], and assessing network formation through nuclear magnetic resonance (NMR). Warpage and stress accumulation during cure are characterized using digital image correlation (DIC) [70, 71], while stress bench setups enable evaluation of stress evolution in processing and service-relevant environments [36]. Multimode Brillouin light scattering (BLS) and low-frequency Raman (LFR) spectroscopy [72] further support local mapping of modulus, viscoelasticity, and phonon interactions.

Ongoing collaborative efforts also explore additional in-situ techniques for tracking cure shrinkage and thermal expansion using DMA [73], TMA [74], and fiber Bragg grating (FBG) sensors [75]. Time-resolved small-angle X-ray scattering (SAXS) offers another powerful in-situ method for probing

polymer network formation and phase evolution. SAXS provides unique insights into chain rearrangement by capturing heterogeneous crosslinking dynamics [76, 77]. It can also characterize processing-induced anisotropy. Ultrasmall angle X-ray scattering (USAXS) can capture the size and spatial distribution of filler particles [78]. These factors are important in managing stress and interfacial adhesion. Through cutting-edge measurement science and collaborative research guided by the CHIPS Metrology Community [79], the NIST project [67] aims to help the industry bridge existing metrology gaps and build a robust foundation for the development and adoption of next-generation packaging materials.

V. OPPORTUNITIES IN MATERIALS AND PROCESSES

The rapid evolution of semiconductor packaging demands innovative materials and processes to address challenges related to warpage and stress, manufacturability, and next-generation device architectures. Here, we highlight several emerging trends discussed during the workshop.

UV-curable resins have emerged as a viable solution to mitigate thermally induced stress and warpage [80]. Their ability to enable localized, low-temperature curing enhances process control and lowers thermal budgets, though broader adoption will require the development of compatible tooling and processing methodologies.

Additively manufactured electronics (AME) are revolutionizing packaging schemes by enabling direct materials deposition with high precision and minimal waste. AME facilitates the fabrication of complex interconnects and custom device architectures but still faces challenges in material compatibility, print resolution, and process standardization [81].

The growing demand for *flexible and stretchable electronics* is driving the development of new polymeric materials with superior flexibility, low dielectric loss, and long-term mechanical stability. These materials are essential for wearable electronics, bio-integrated devices, and flexible displays, requiring careful consideration of adhesion, fatigue resistance, and environmental durability [82, 83].

Ultra-low D_k and D_f dielectrics for 5G/6G applications are emerging [84, 85]. In antenna-in-package (AiP) designs, low D_k enhances signal speed and antenna gain, while low D_f reduces dielectric loss and improves radiation efficiency. Achieving these properties requires precise molecular engineering, creating new opportunities to develop novel, low-loss dielectric polymers tailored for high-frequency electronics.

Dynamic thermosets, or vitrimers, are gaining attention for their unique ability to undergo network rearrangement without breaking down. These novel materials offer potential for stress relaxation to mitigate warpage in high-density packaging and offer recyclability benefits for sustainable packaging [86, 87].

Lastly, *next-generation TIMs* for electric vehicles and autonomous systems are being developed to deliver high thermal conductivity and long-term reliability under harsh conditions [88]. Current research is increasingly focused on

integrating nanostructures to meet stringent thermal management requirements of compact, high-power electronics [89]. High-temperature aging or degradation of encapsulation materials in electronic components used in *automotive* applications is another reliability concern during long-term use [90].

VI. RESEARCH-GRADE TEST MATERIALS (RGTMS)

The path toward HI is not only diverse in the materials and metrology being explored, but also in the tailored approaches adopted across different sectors of the semiconductor ecosystem—each requiring unique solutions to address distinct challenges. A practice or solution that works well for one company, one application, or one process may not be directly transferable to another. Moreover, knowledge gained from one material or process often lacks generalizability due to the proprietary nature of commercial materials, which remain "black boxes" in terms of composition and additives. This makes quantitative comparisons between different systems difficult. In this dynamic, fast-paced landscape, RGTMs [91], a class of Reference Materials (RMs) [92], can serve as a powerful vehicle for next-generation packaging development. These well-characterized, non-proprietary model materials serve as industry benchmarks, enabling consistent measurements, supporting the development of standardized methodologies, and advancing the scientific understanding of structure-property relationships in packaging materials.

NIST is leading efforts to develop RGTMs specifically tailored for semiconductor packaging applications, with initial focus on advanced underfill materials and molding compounds. Unlike commercial materials, RGTMs offer a transparent and reproducible foundation for evaluating material properties and establishing consistent measurement protocols. These efforts aim to standardize metrologies, improve interlaboratory reproducibility, and facilitate the development of open-access materials databases for broader industry adoption.

The development of RGTMs at NIST is tightly coupled with the advancement of cutting-edge metrology techniques capable of probing structure-property relationships with spatial, temporal, and chemical resolution not previously accessible in semiconductor research. This unique "one-two punch"—combining metrology innovation with model material development—is a hallmark of NIST's approach to advancing packaging science. This integrated strategy ensures that data generated from RGTMs is not only reproducible but also actionable, enabling the calibration and validation of predictive models for materials and process optimization.

"Fundamentals don't change. Only difference is scale."

—Jeff Gotro, Innocentrix, LLC, at the 2024 Electronics Packaging Symposium By utilizing simple, well-defined model systems, NIST's RGTM approach enables the systematic investigation of key variables—such as resin chemistry and filler content—providing fundamental insights applicable across different material classes and packaging architectures. While the industry continues to evolve in complexity, the underlying material science principles remain unchanged—only the scale and integration challenges have changed from yesterday to today.

The benefits of RGTMs extend beyond metrology: they accelerate materials innovation, support technology qualification, and enhance industry collaboration. By enabling a data-driven approach to material characterization, RGTMs promote open science and transparency. This, in turn, helps individual companies optimize their materials and processes and also supports the broader semiconductor ecosystem by aligning efforts between industry, academia, and government research organizations.

VII. SUMMARY AND OUTLOOK

The landscape of polymeric materials for advanced semiconductor packaging faces a host of significant challenges. First, the industry's reliance on overseas polymer suppliers poses vulnerabilities in supply chain resilience and innovation security. Closely related to these concerns is the rapidly changing regulatory climate that can cause manufacturers to modify material formulations or withdraw products on short notice. Regulatory actions in one region can have global repercussions, affecting material availability worldwide. Second, limited information on material composition and performance data—due to proprietary constraints—complicates material selection and qualification processes. Furthermore, current characterization standards metrology methods lack consistency and comprehensiveness, often proving inadequate for evaluating novel, complex material formulations.

To address these challenges, key takeaways from the panel discussion point to several recommendations. semiconductor packaging community must come together to: (1) re-establish US-based supply chains for polymeric materials to reduce dependency on offshore suppliers; (2) improve transparency in material data sheets and promote standardized guidelines for material characterization; (3) develop advanced metrologies that meet stringent and evolving performance requirements of next-generation materials; and (4) foster robust, pre-competitive research collaboration among academia, government laboratories, and industry to accelerate materials development and adoption. With emerging materials requiring more than 10 years and up to 25 years to reach volume production [93], sustained collaborative efforts, such as those led by the CHIPS Metrology Community [94], are essential for driving longterm industry growth and competitiveness.

Addressing the intrinsic polymer challenges in semiconductor packaging requires a deep, fundamental understanding of polymer science. Expertise in this field is crucial for elucidating the complex relationships among material constituents, bulk and interfacial properties,

processing conditions, and final material performance and reliability. As the semiconductor industry continues to push the boundaries of packaging technology, collaboration among material scientists, process engineers, and metrology experts will be essential. Advancements in measurement science will be critical to unlocking and accelerating the next generation of high-performance, reliable, and sustainable materials and processes.

Central to this effort is NIST's unique "one-two punch" strategy: the integration of metrology development with the implementation of rigorously characterized model materials. With its mission-driven mandate to pursue rigorous, foundational work, NIST is uniquely positioned to develop the advanced metrology for tomorrow's packaging materials [13]. These efforts aim to establish measurement methods that are both precise and practical. Ongoing work [36, 37, 72] is already yielding progress in characterizing the structure, chemistry, and thermomechanical behavior in highly filled, heterogeneous polymer systems—enabling insights that would be difficult to obtain through industrial R&D alone.

The strategic development and application of Research Grade Test Materials (RGTMs), are crucial for benchmarking, developing metrology methods, and elucidating fundamental structure-property-processing relationships in highly-filled polymeric materials. As the semiconductor industry continues to push the limits of 3D heterogeneous integration, the role of RGTMs will become even more critical. Ultimately, RGTMs help both individual companies and the broader semiconductor ecosystem by aligning measurement practices, simulation inputs, materials development strategies and qualification processes—paving the way for a more resilient, efficient, and collaborative framework for semiconductor innovation.

Standardizing material property reporting and enhancing data transparency, including recommendations for datasheets and measurement protocols, are critical for improving material selection and reliability prediction. Future metrology strategies must integrate multimodal measurement approaches and support open-access databases [95] to facilitate broad industry participation. Establishing a material property database that adheres to Findable, Accessible, Interoperable, Reusable (FAIR) principles, and includes Machine Learning (ML)-ready datasets, along with standardized test methods, enable accurate modeling and simulation semiconductor packaging technologies.

By unifying these strategies through coordinated collaboration among academia, government, and industry—and aligning with initiatives such as the Heterogeneous Integration Roadmap (HIR) [5] and the Microelectronics and Advanced Packaging Technologies (MAPT) Roadmap [96]—will help position the U.S. to maintain its technological leadership and build a more robust and resilient semiconductor ecosystem.

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DISCLAIMER

These opinions, recommendations, findings, and conclusions do not necessarily reflect the views or policies of NIST or the United States Government.

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